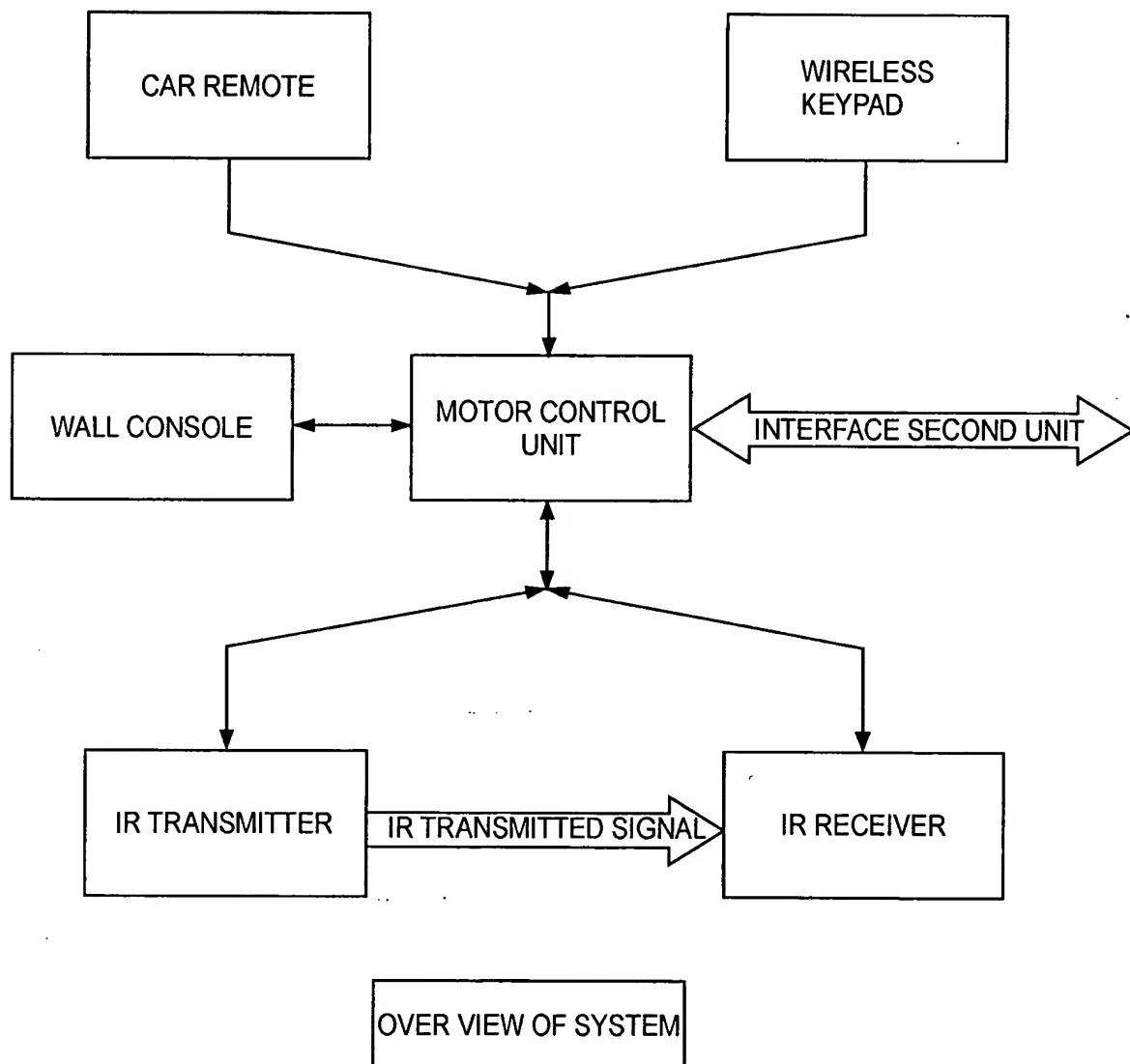




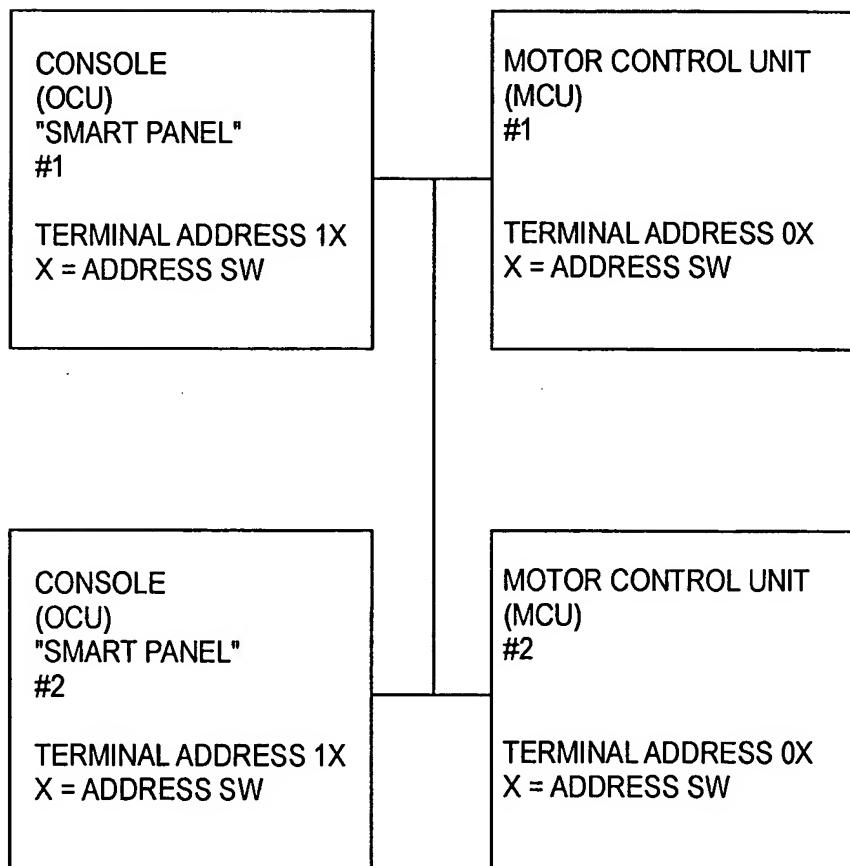
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Fig. 1



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Fig. 2



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Fig. 3

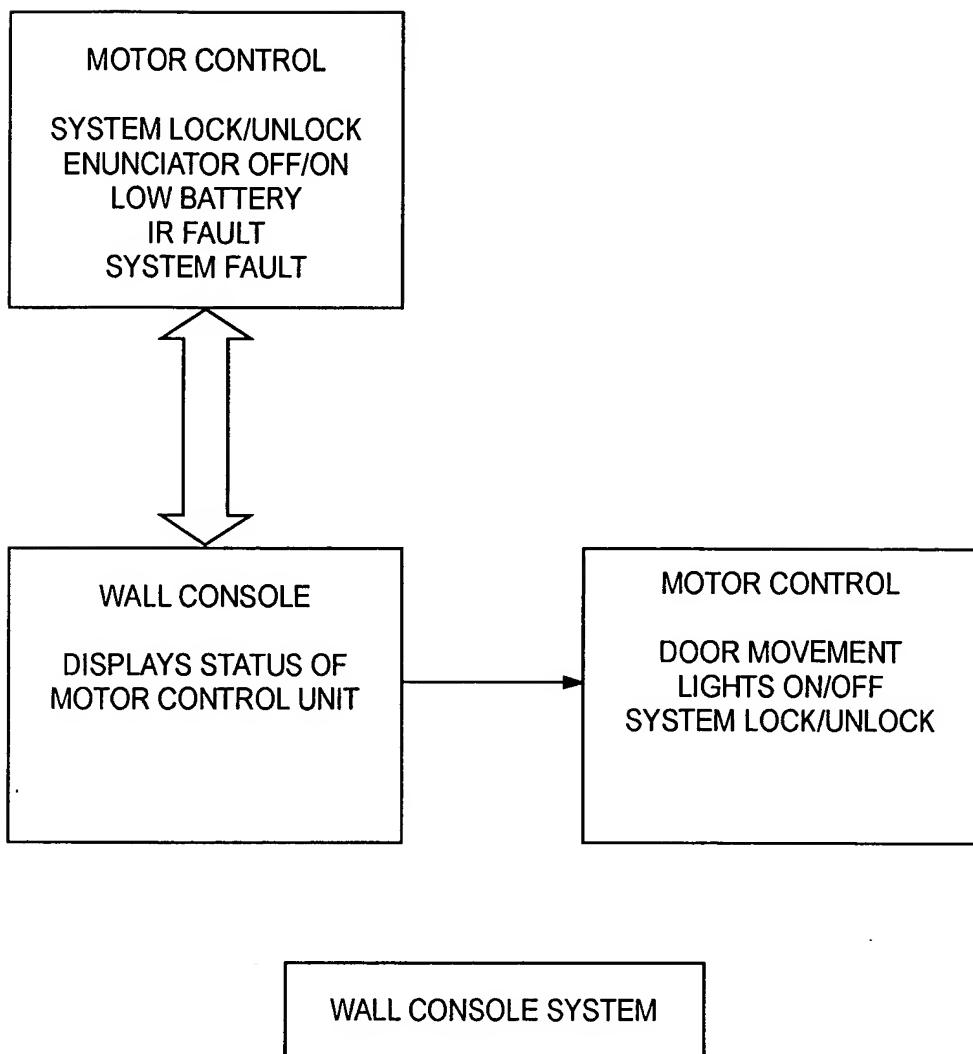
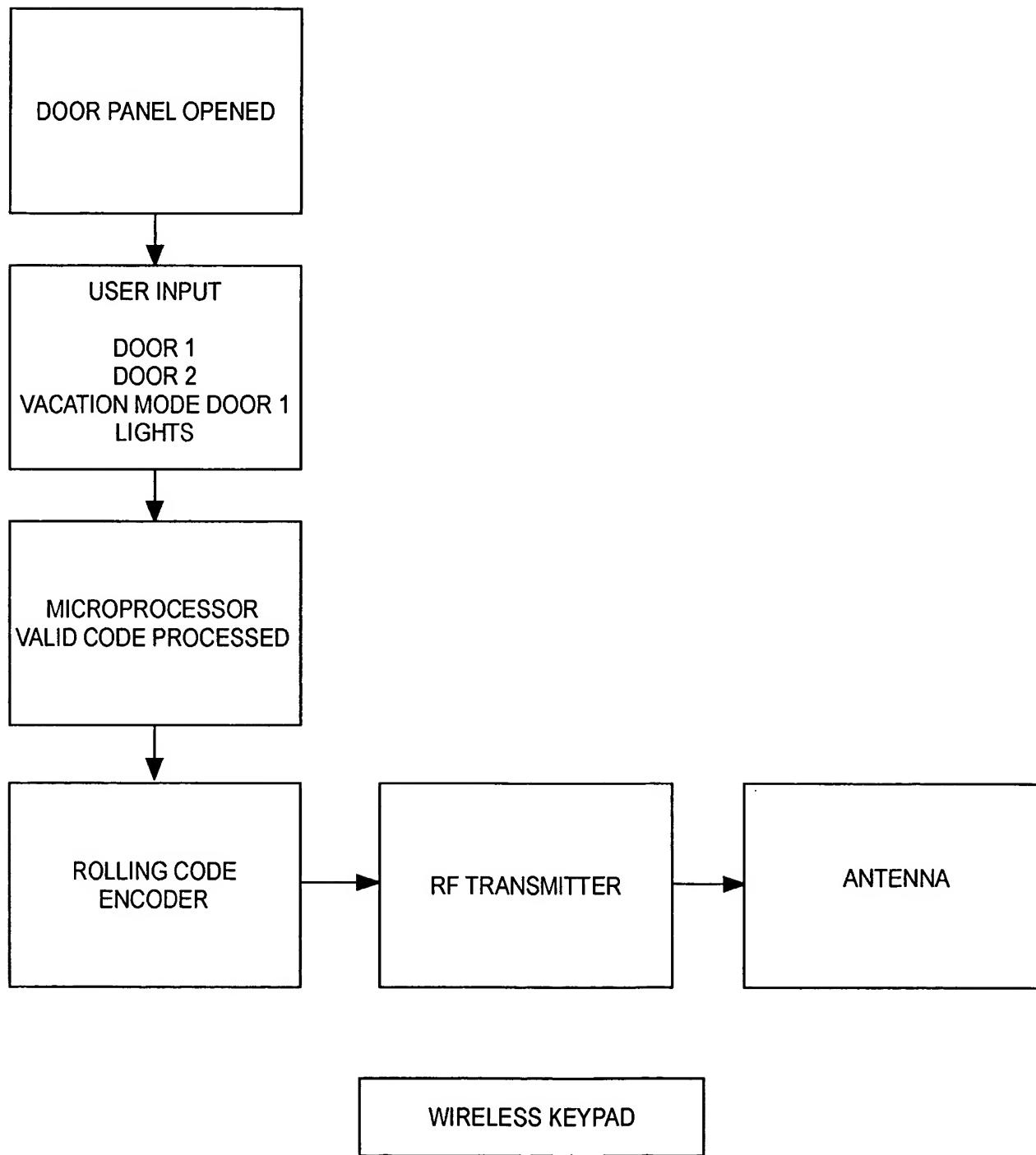
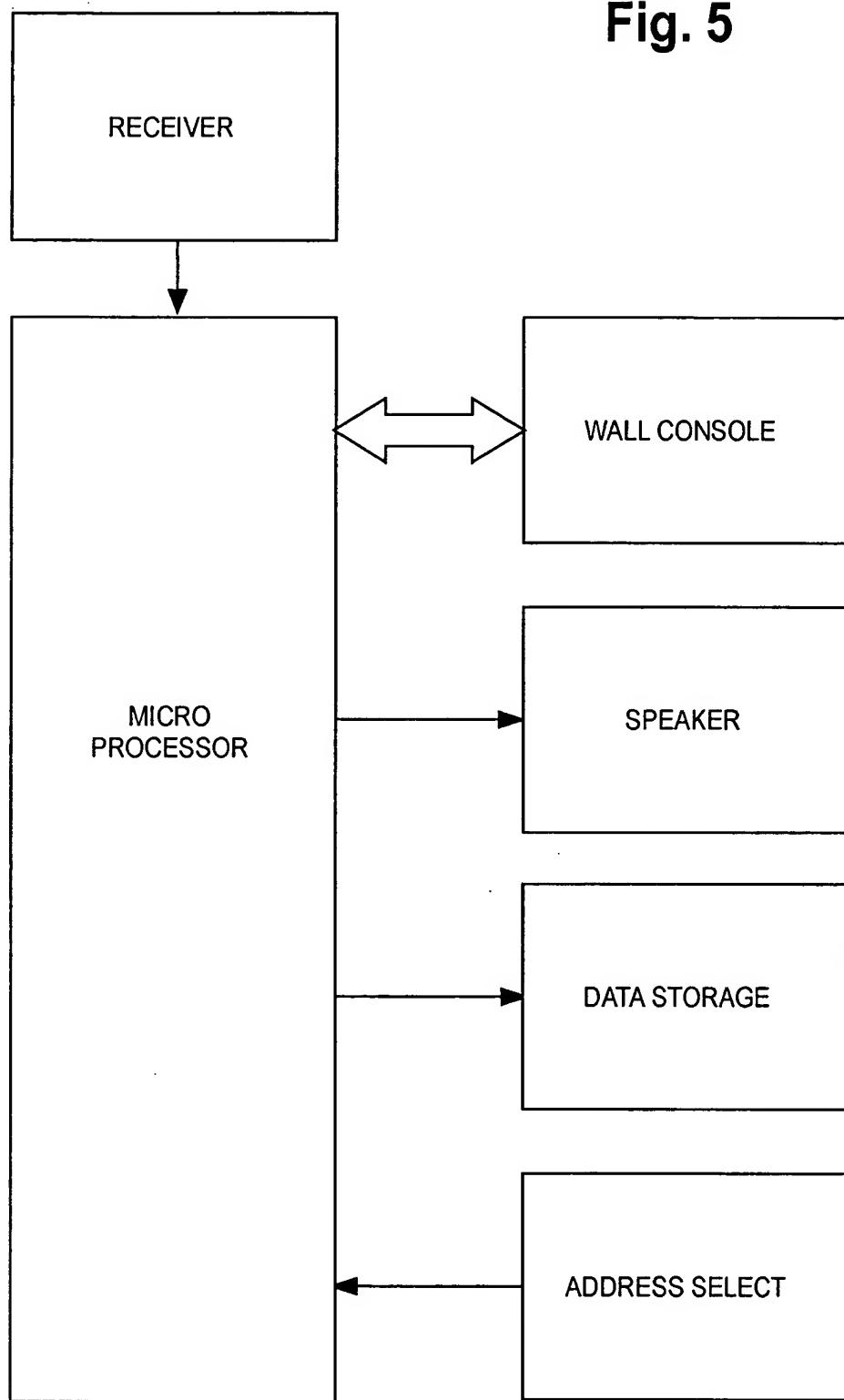


Fig. 4



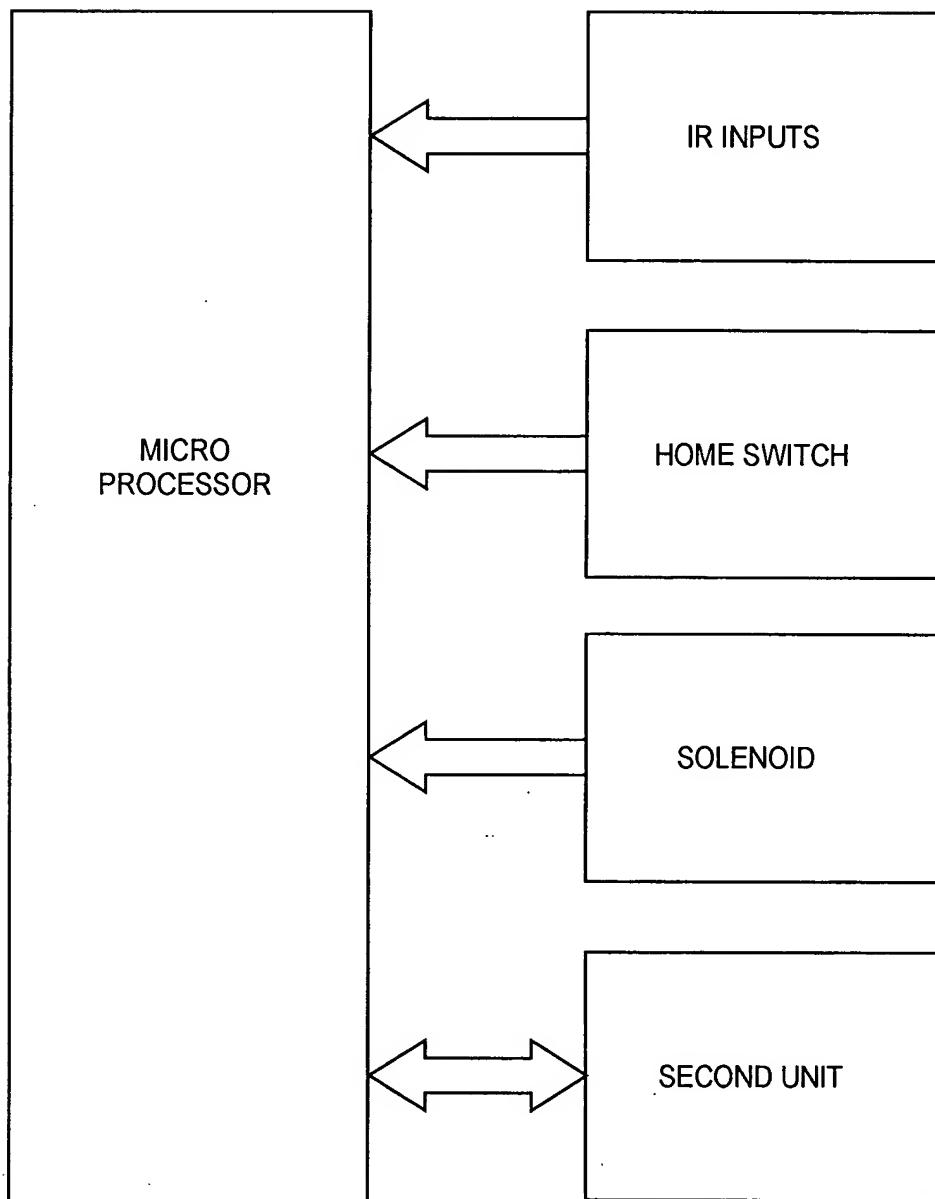
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Fig. 5



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Fig. 6



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Fig. 7

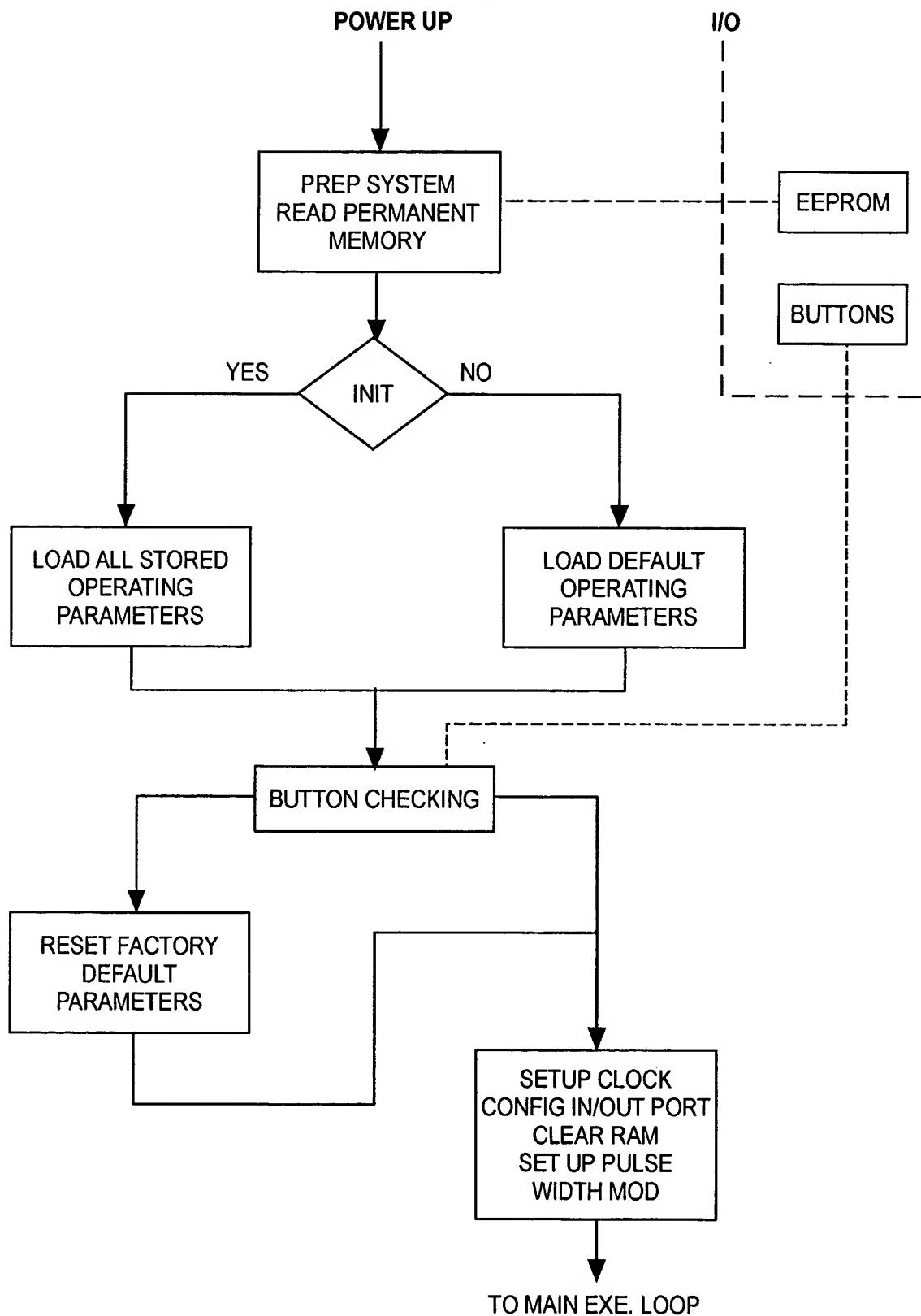


Fig. 8

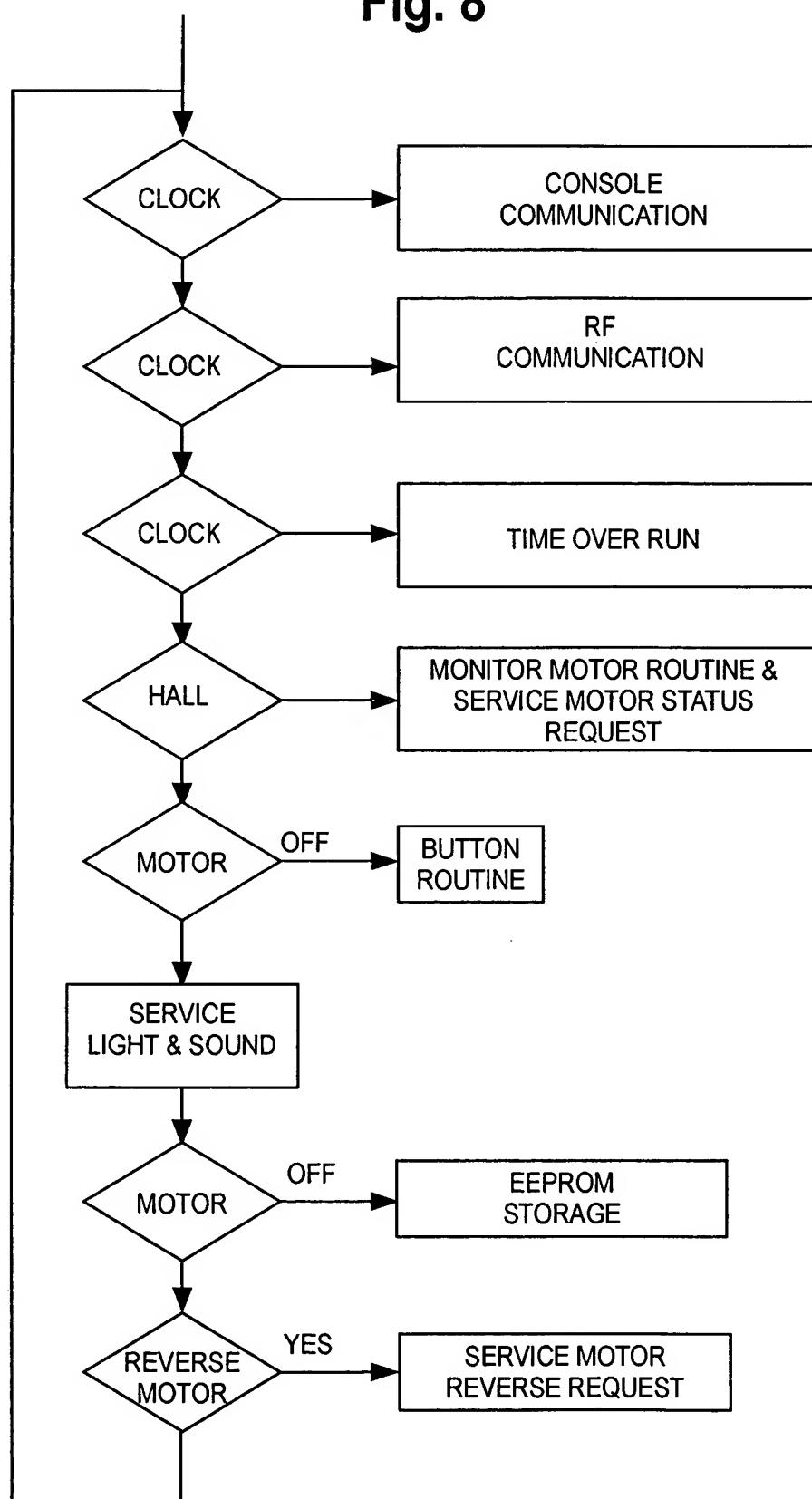


Fig. 9

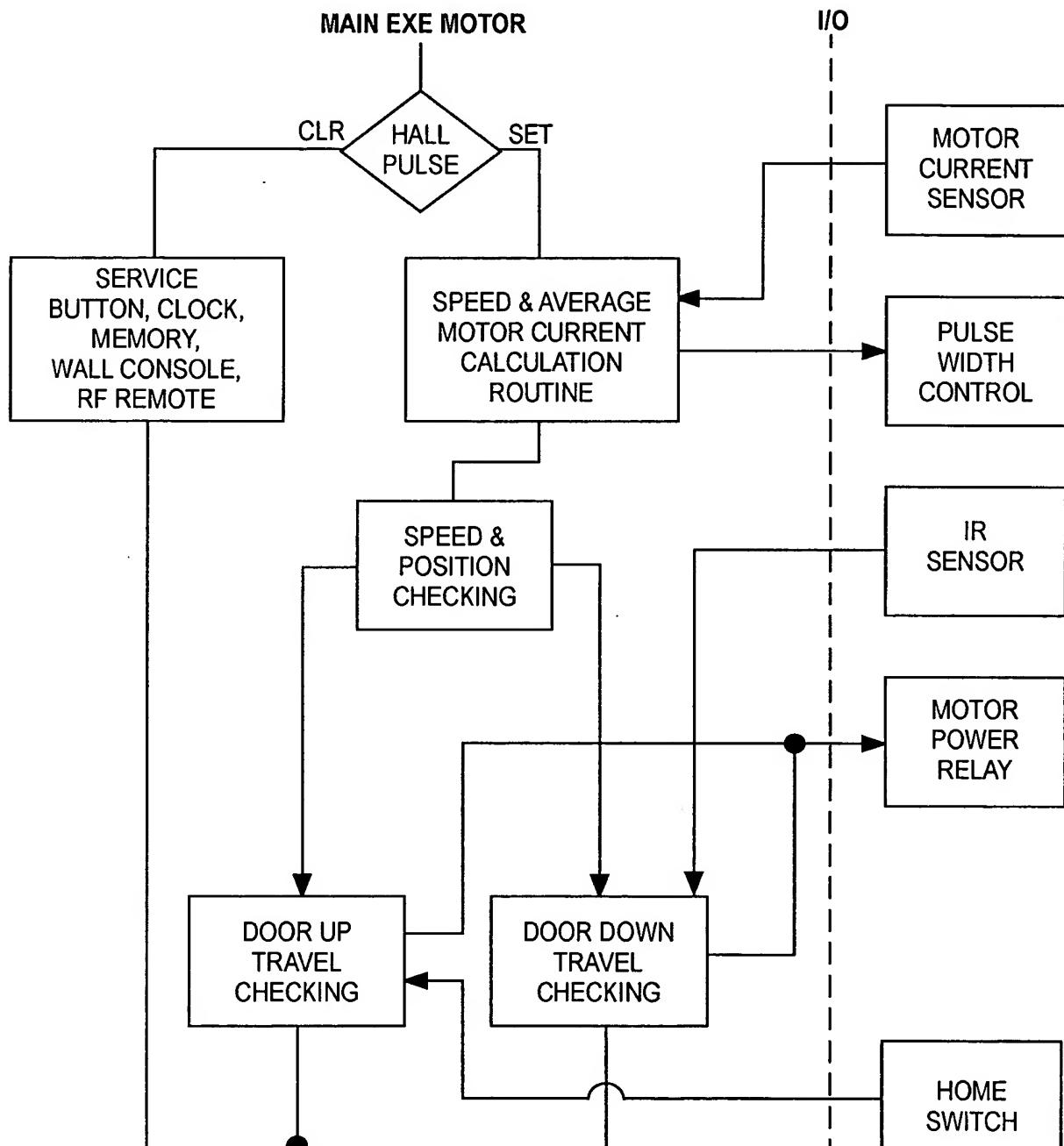
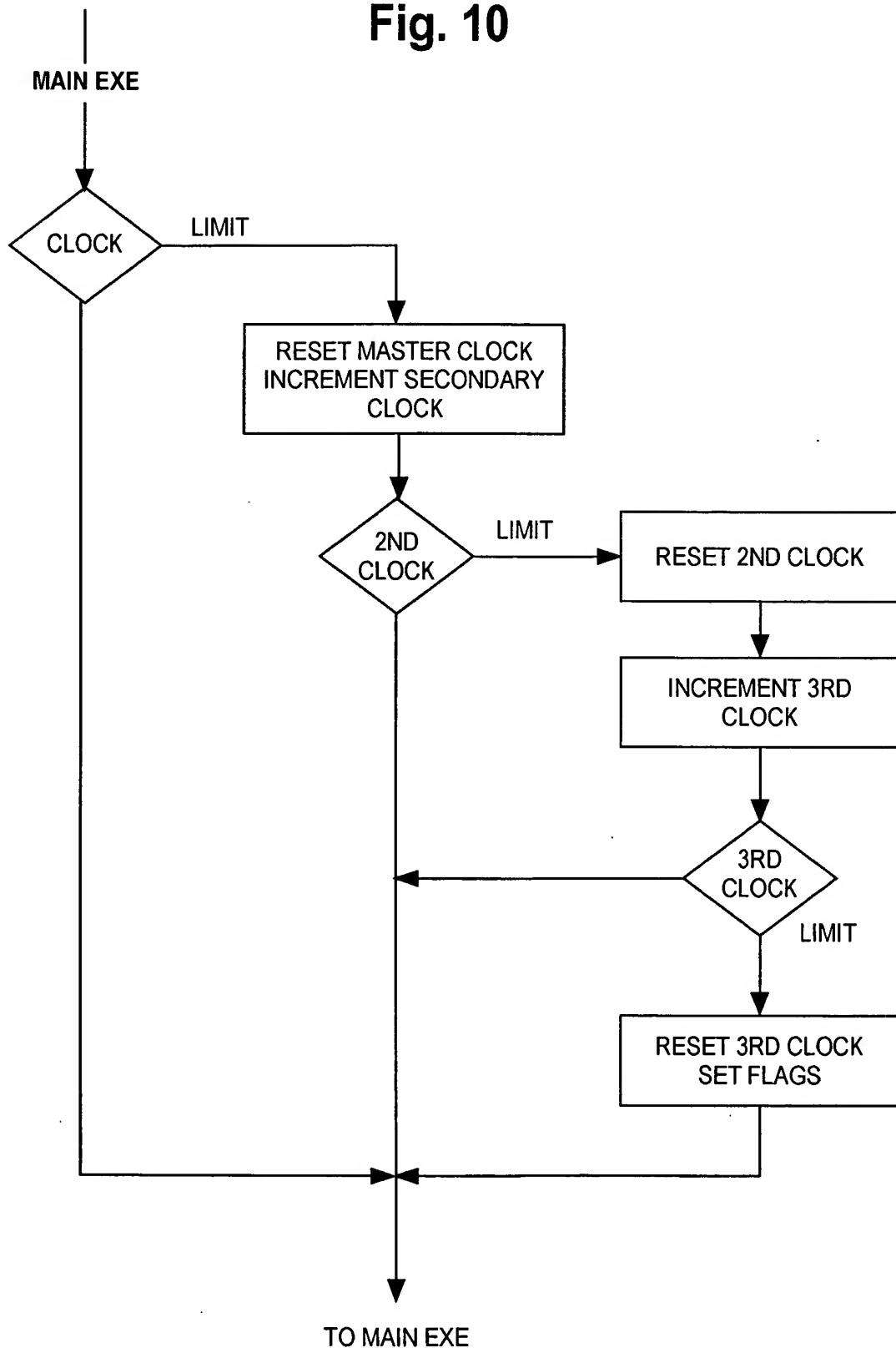


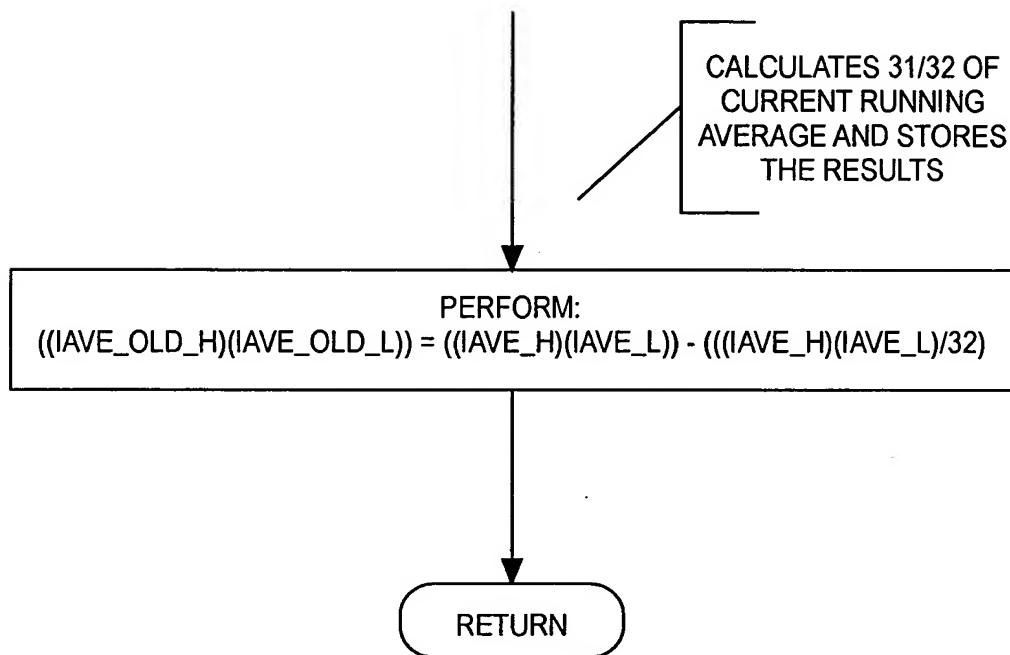
Fig. 10



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Fig. 11

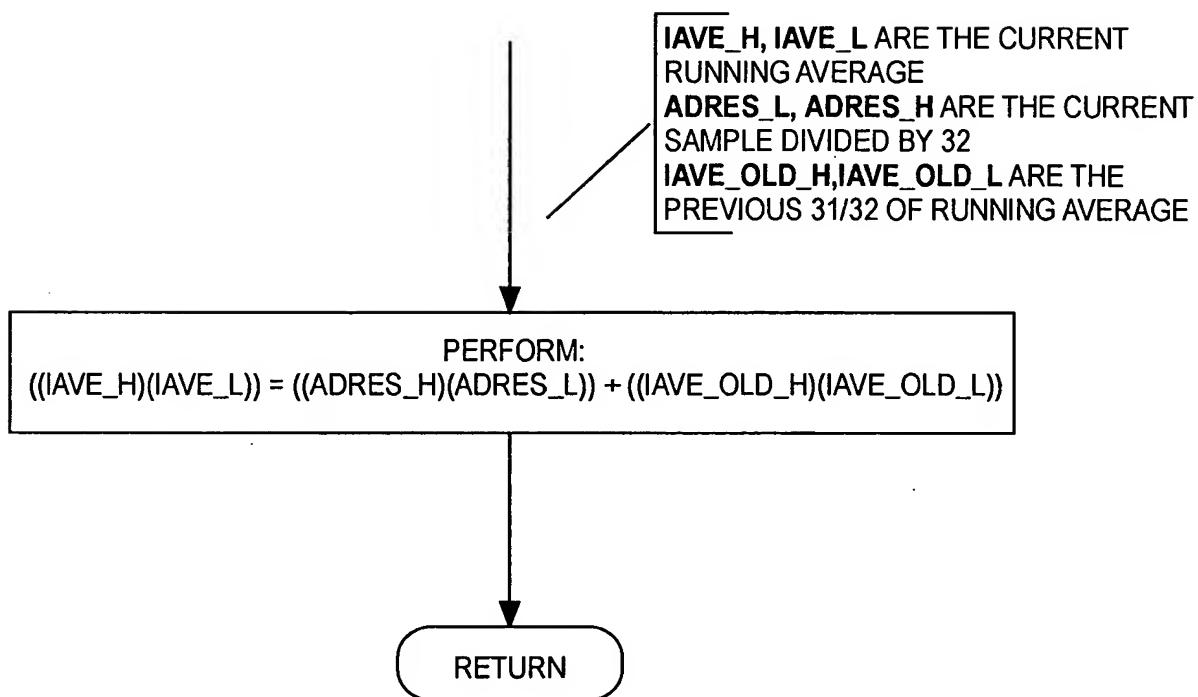
CALC_OLD_AVE



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Fig. 12

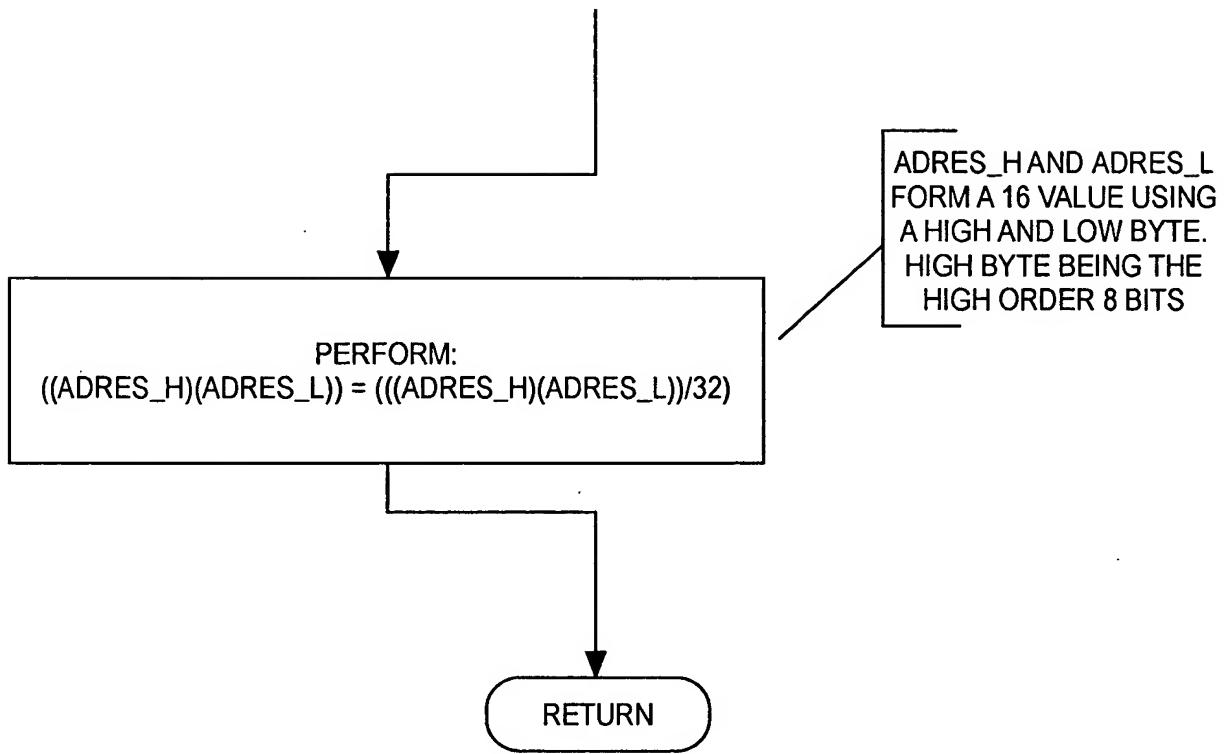
CALC_NEW_AVE



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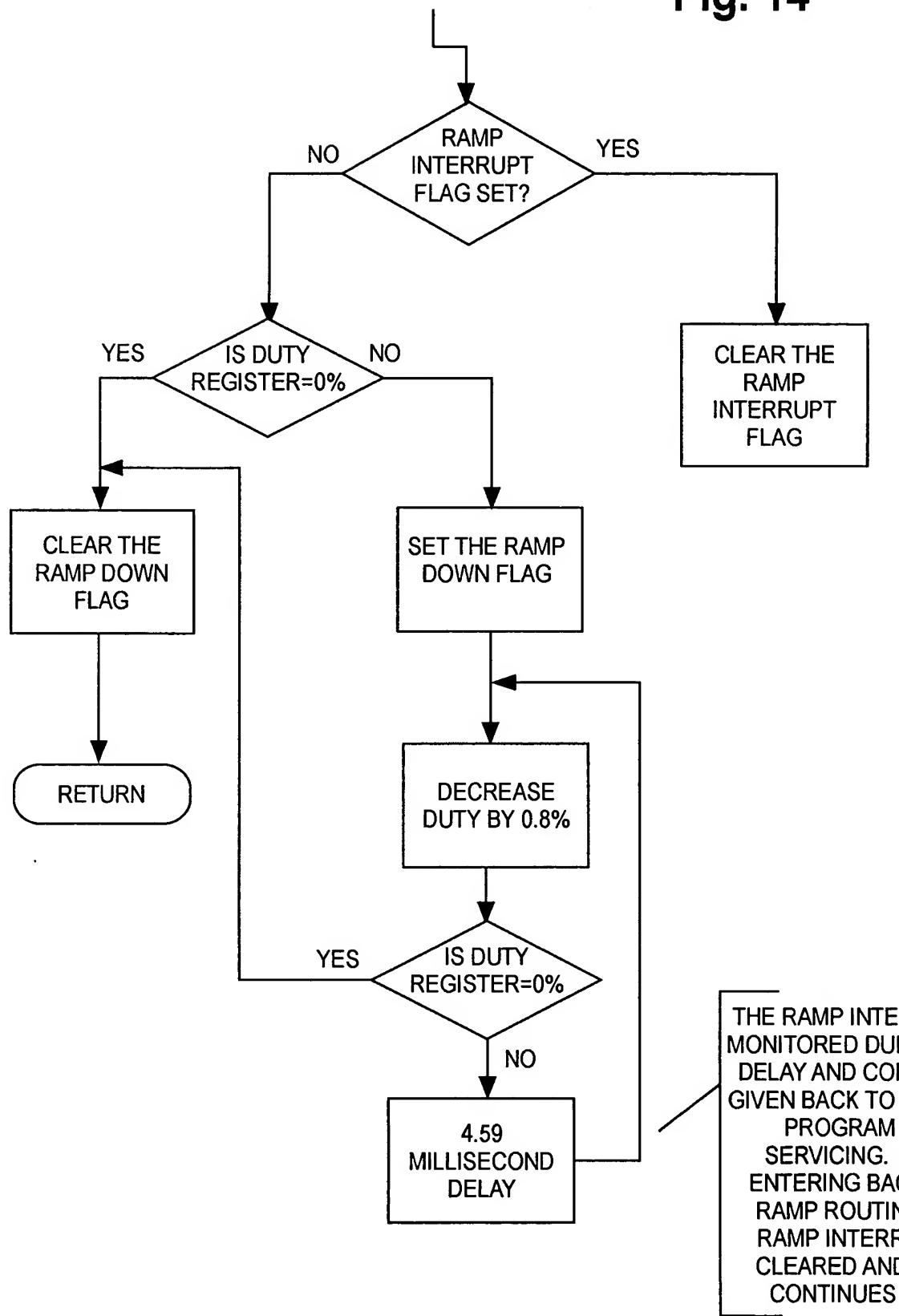
Fig. 13

AD_DIV_32



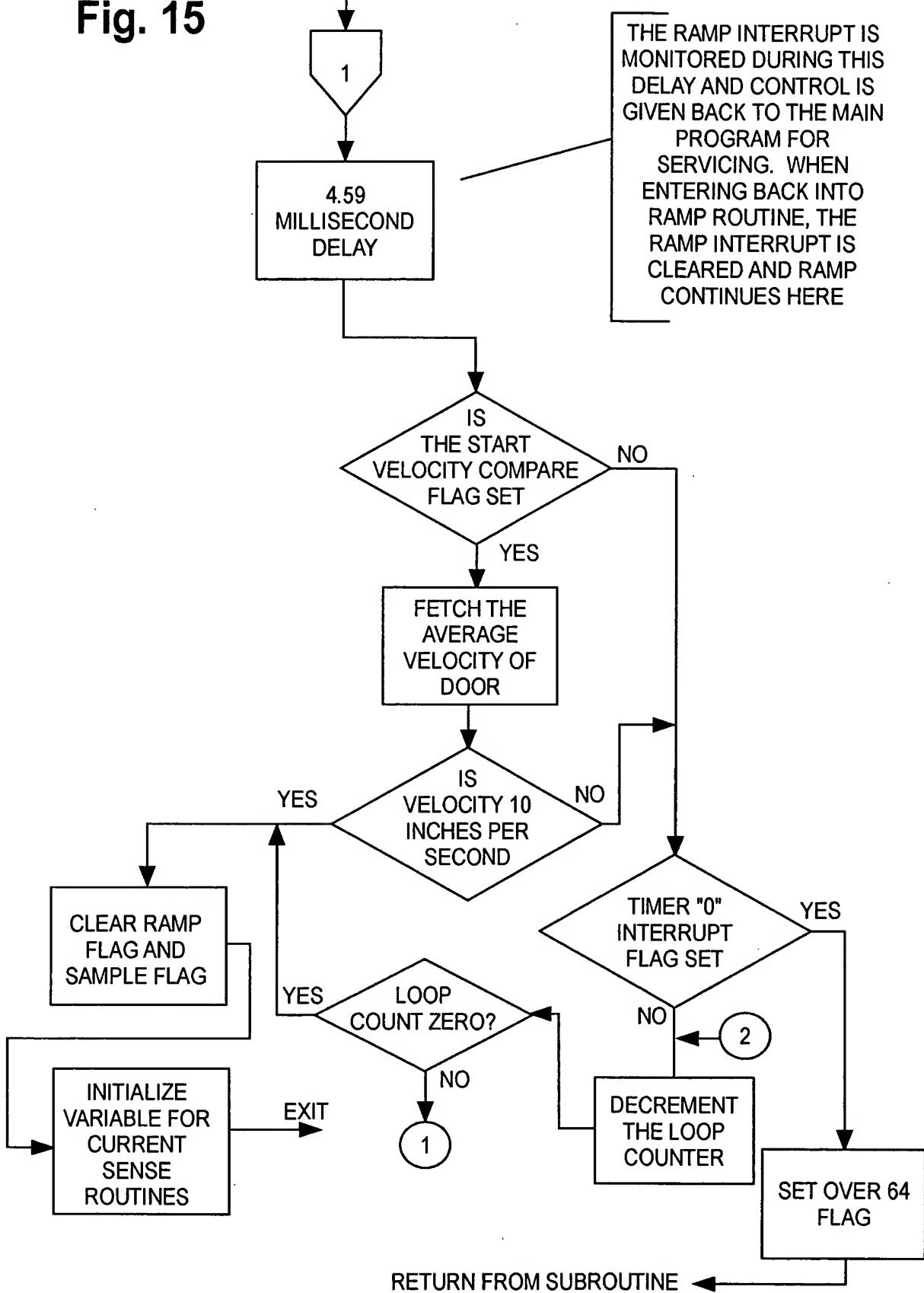
PWMRAMPDOWN

Fig. 14



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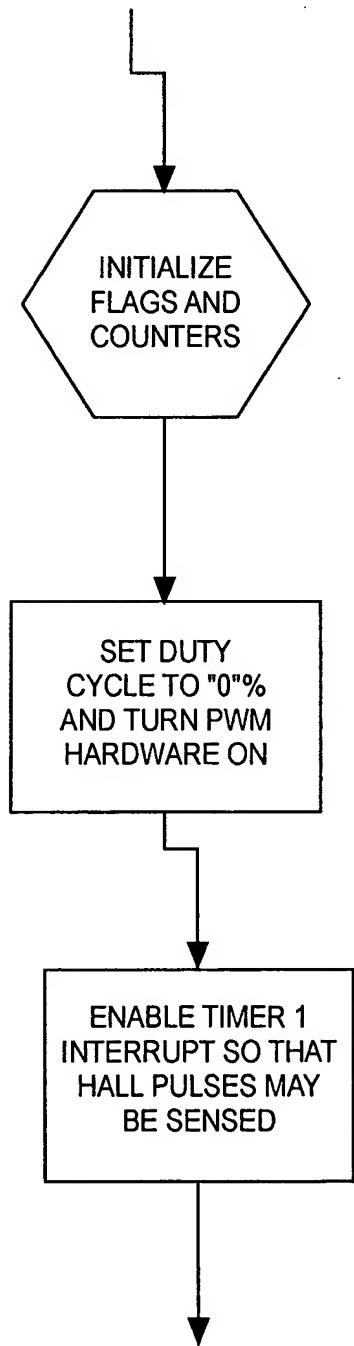
Fig. 15



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Fig. 16

PWM_RAMP_INIT



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Fig. 17

PWM_HW_INIT

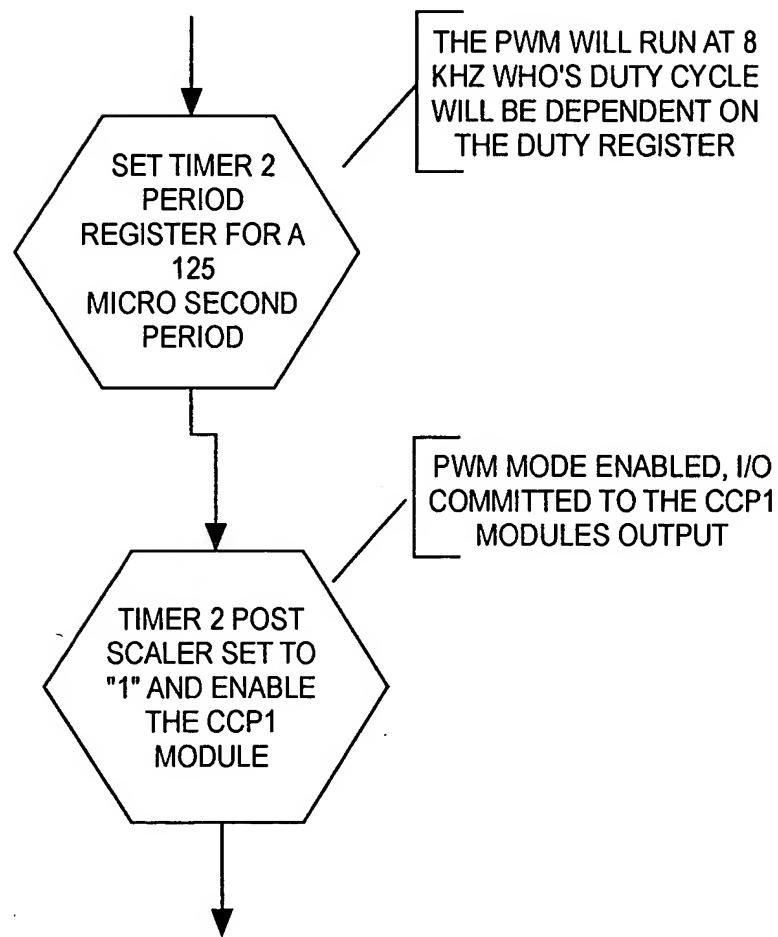
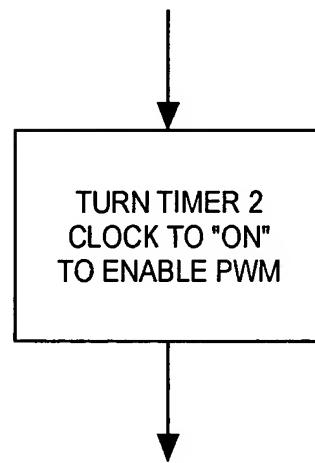
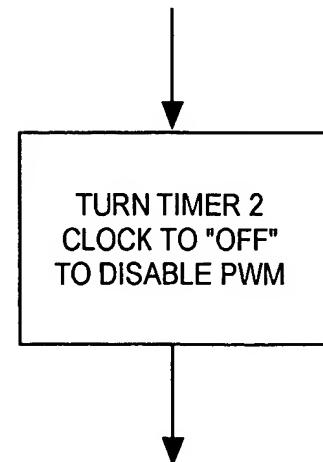


Fig. 18

PWM_HW_ON



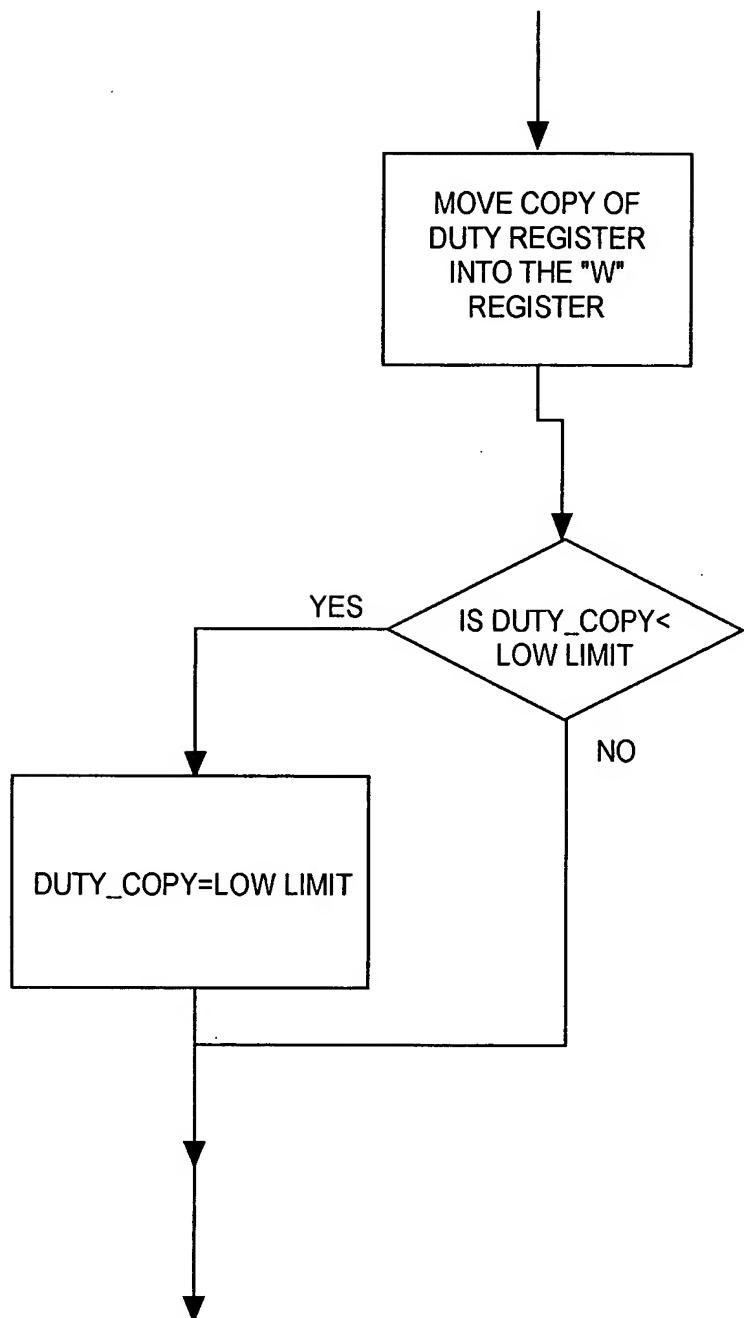
PWM_HW_OFF



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Fig. 19

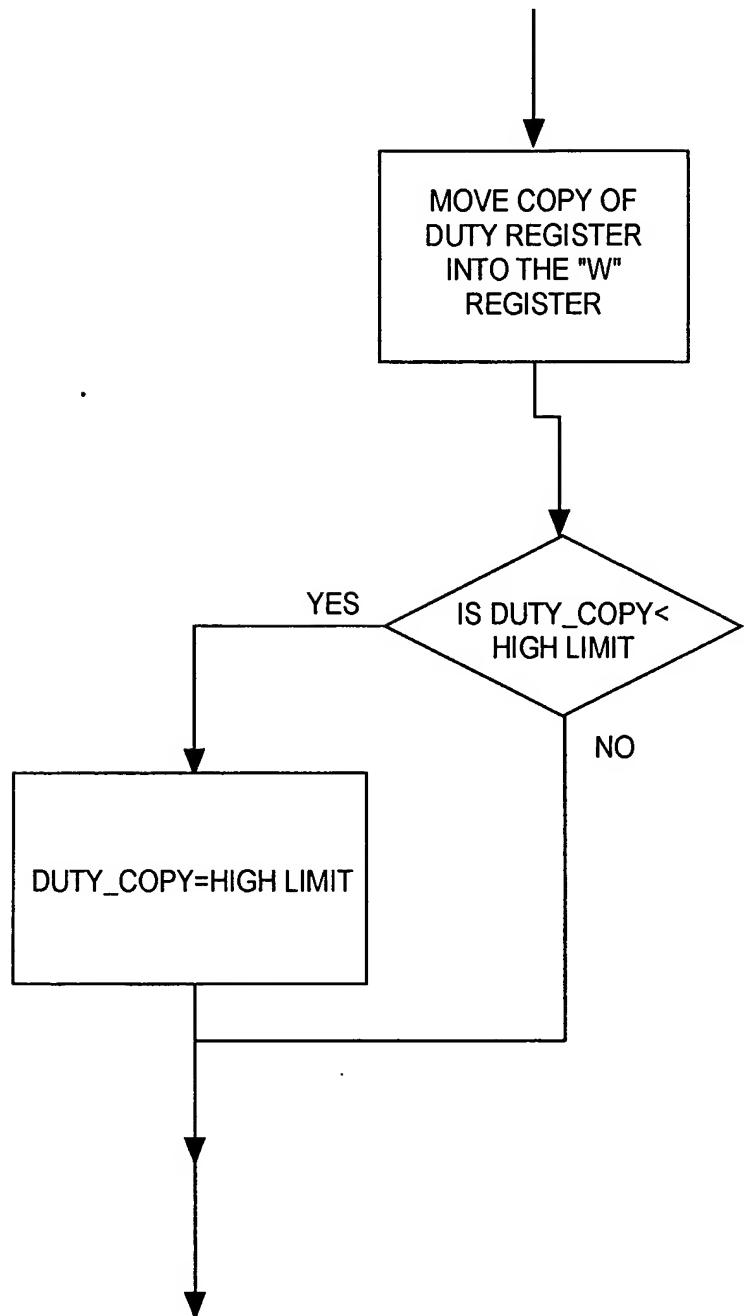
LOW_CLIP



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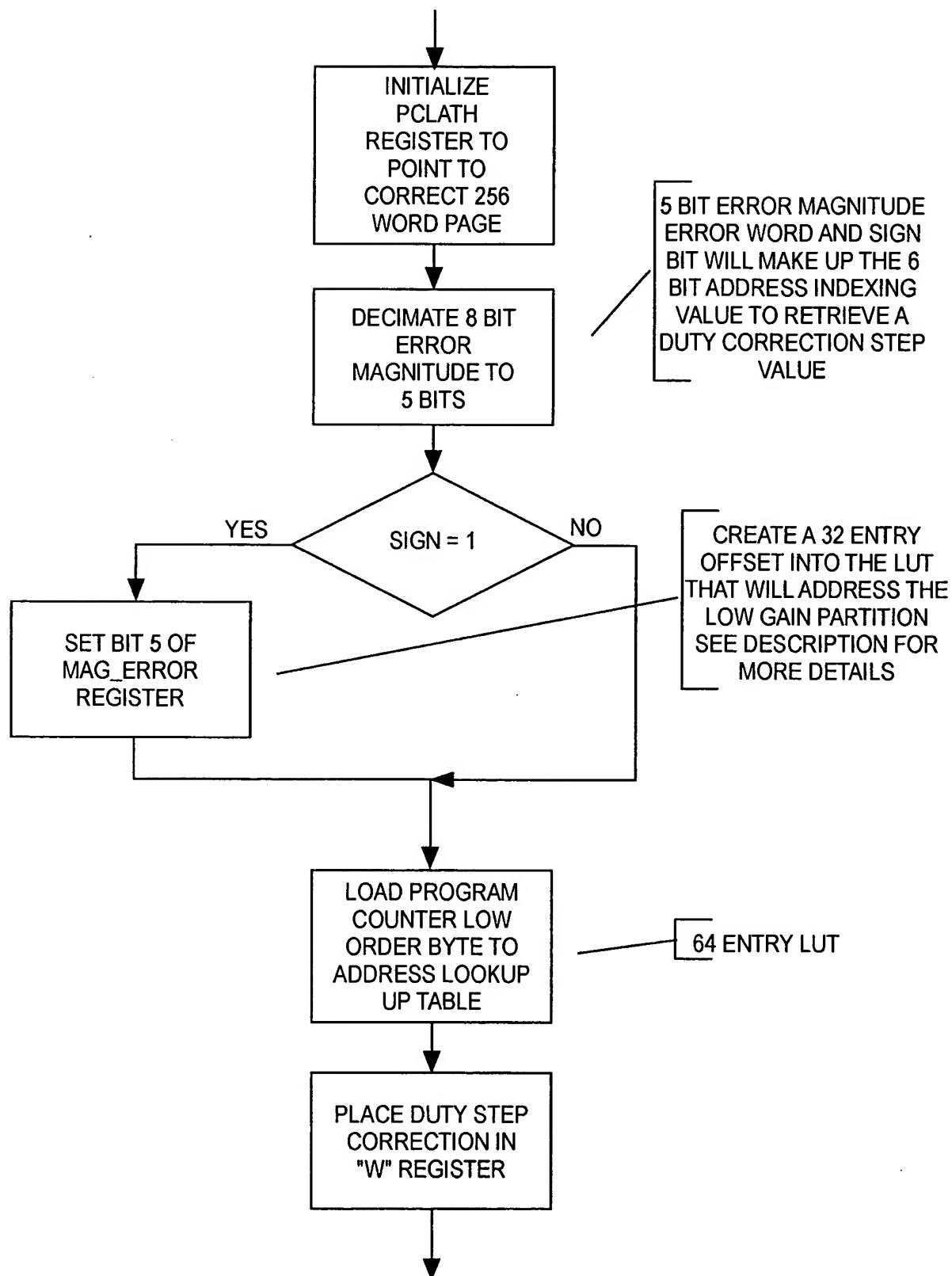
Fig. 20

HIGH_CLIP



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Fig. 21 PWM_GAIN



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Fig. 22

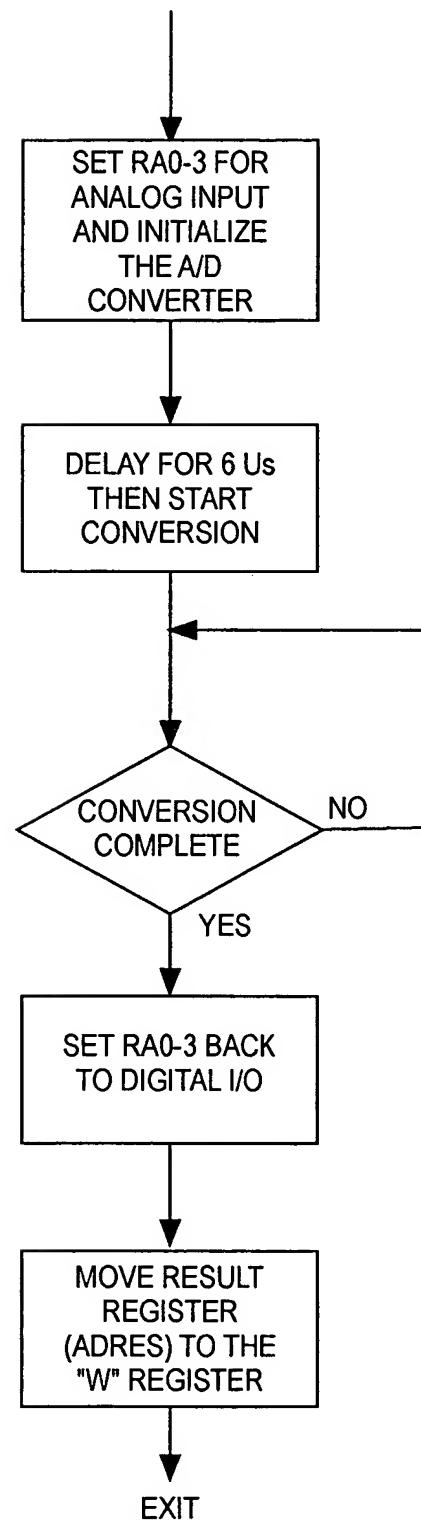


Fig. 23

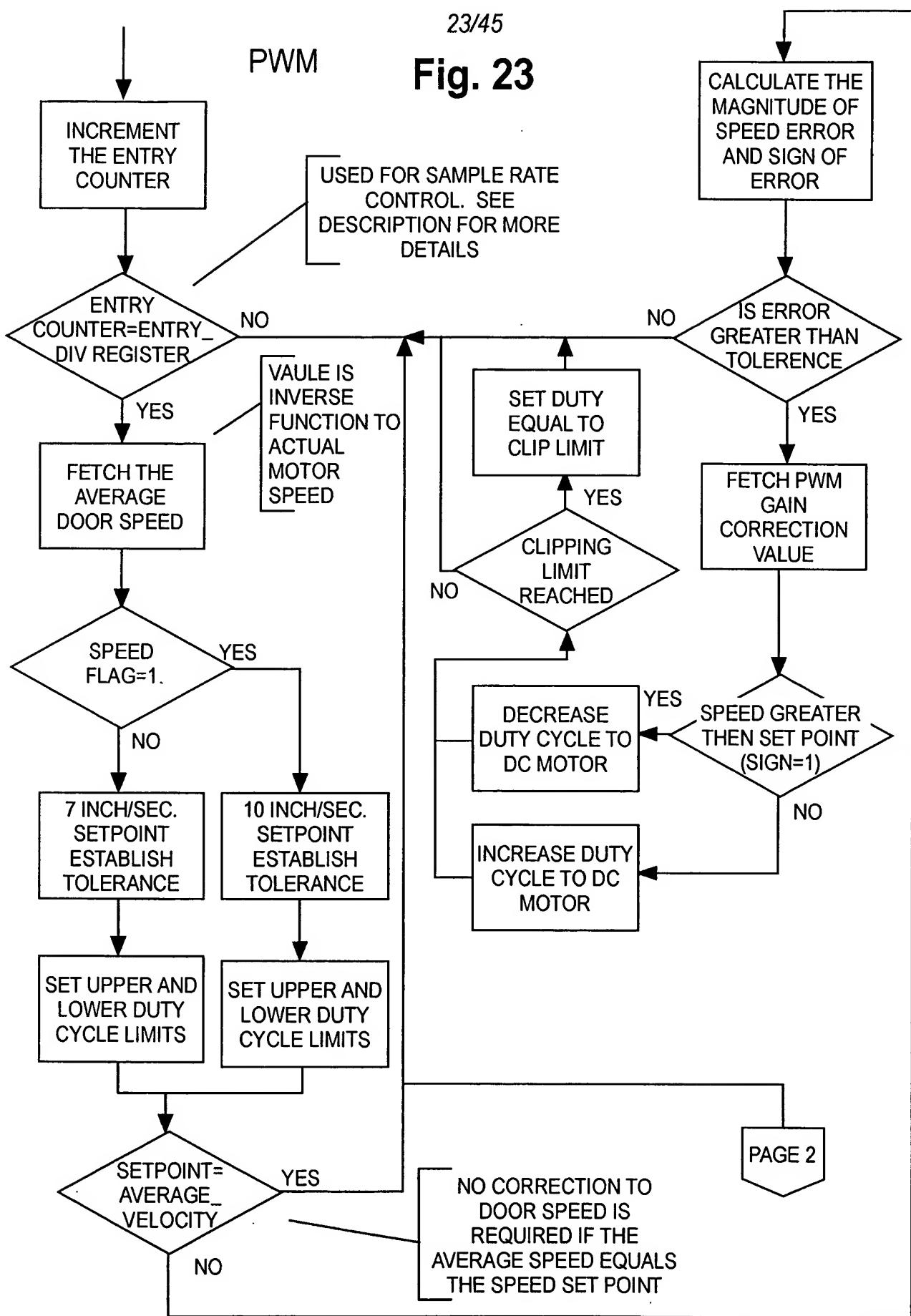


Fig. 24

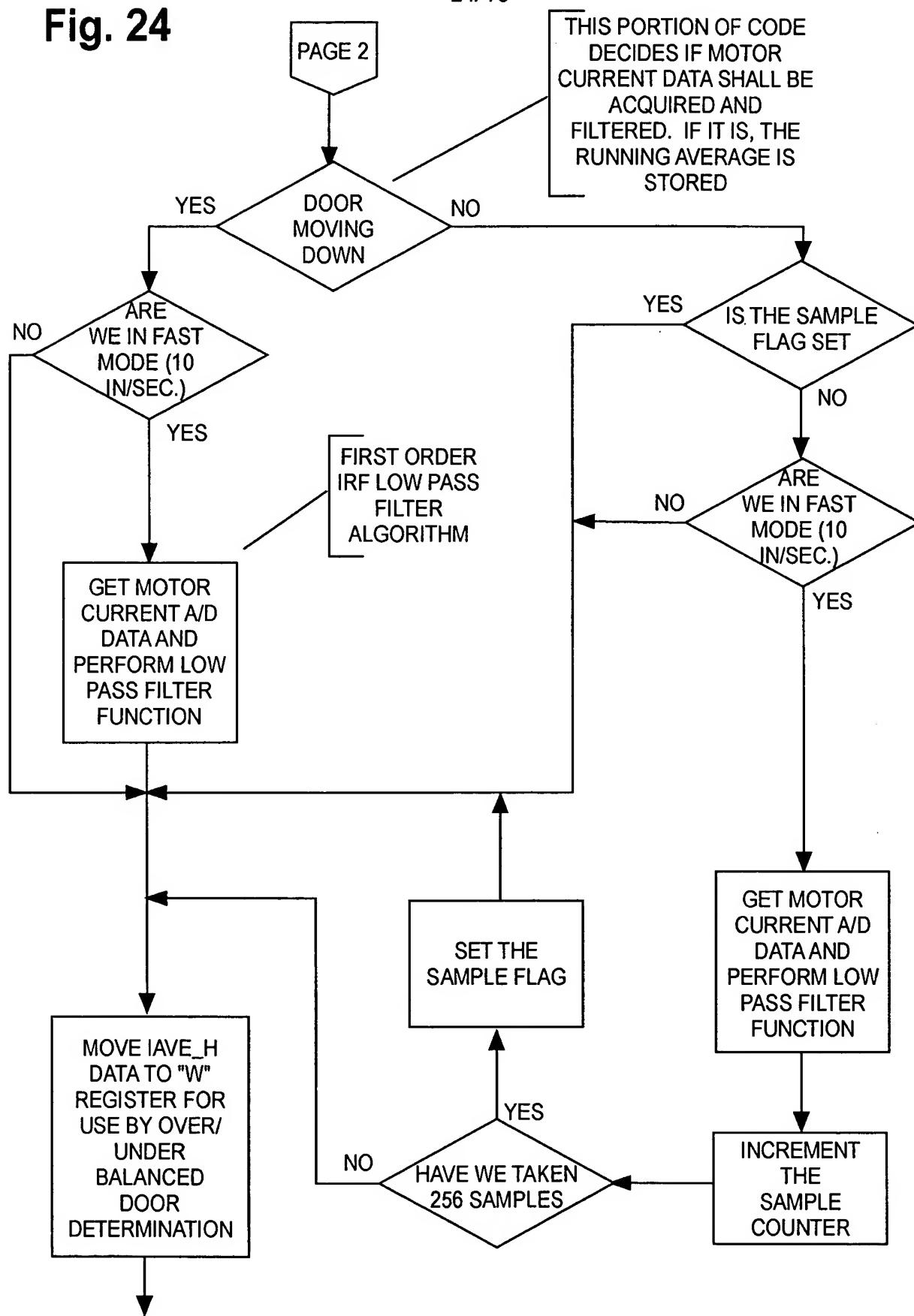


Fig. 25

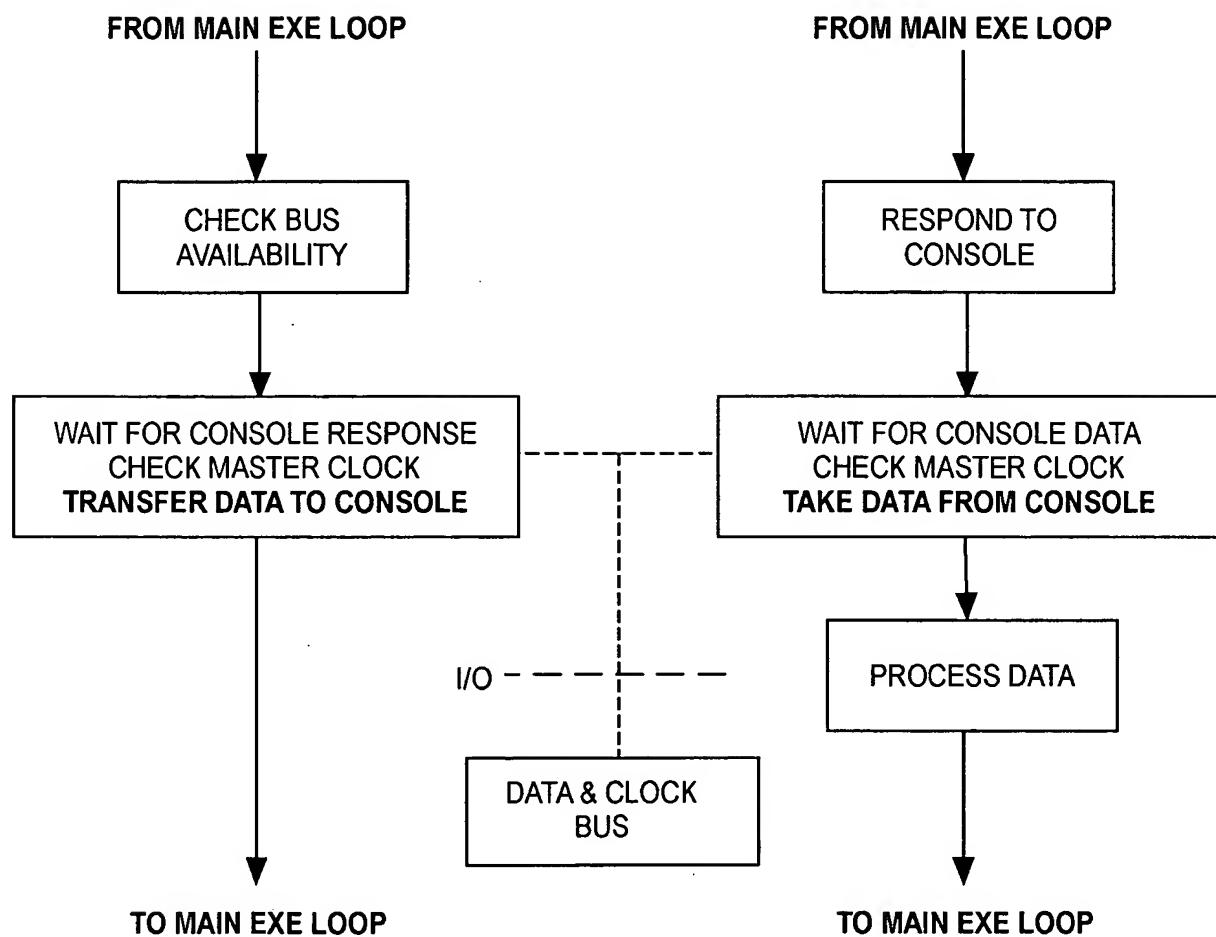


Fig. 26

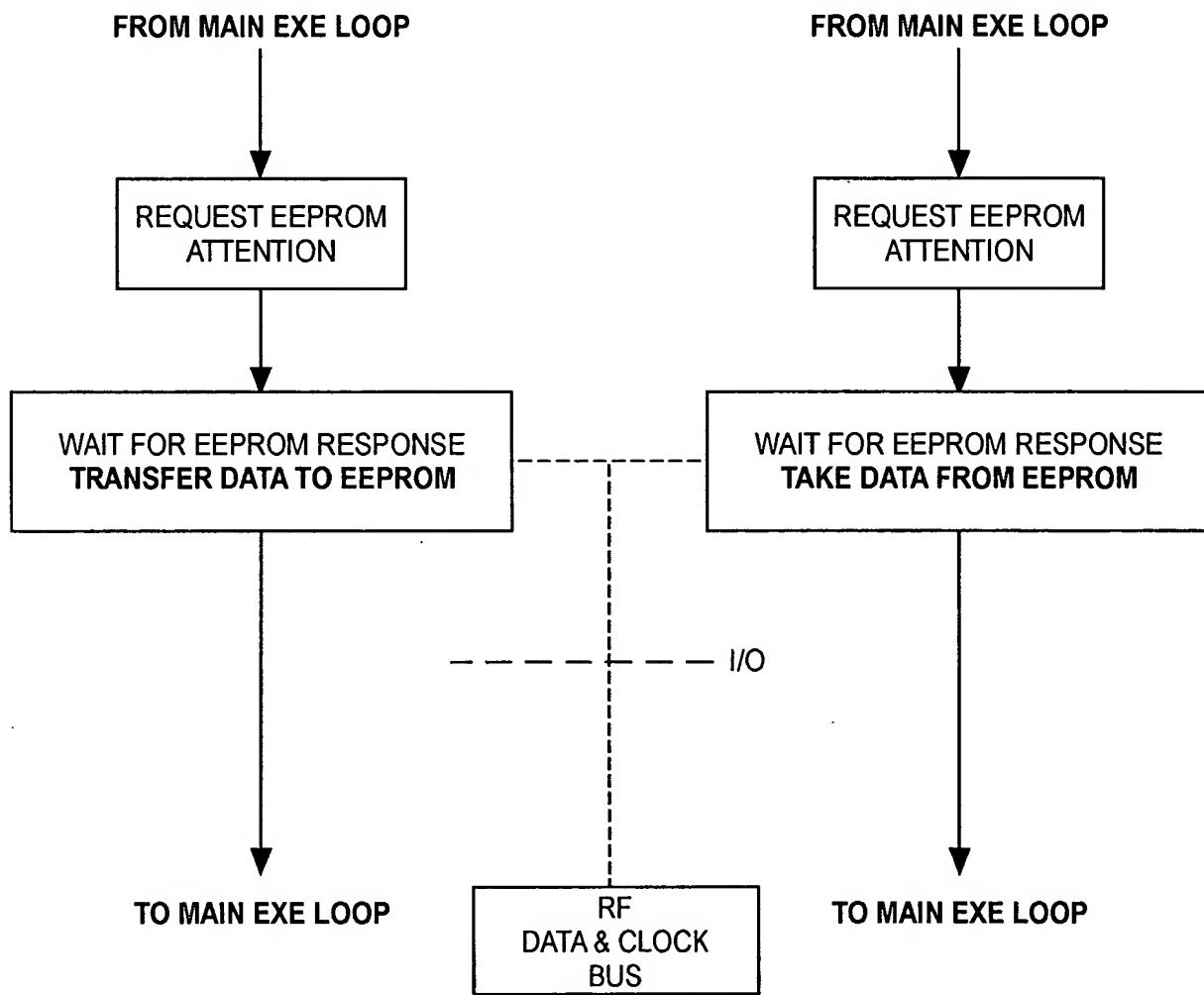


Fig. 27

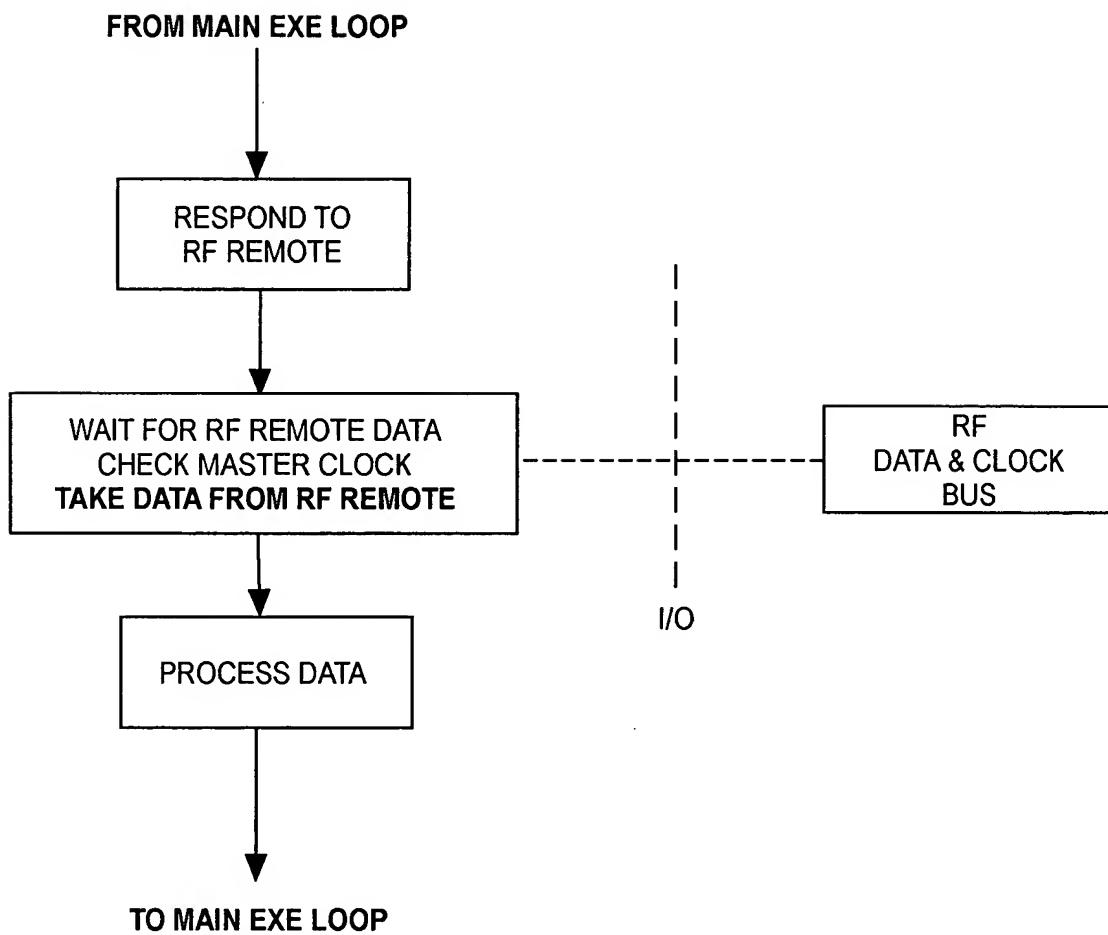
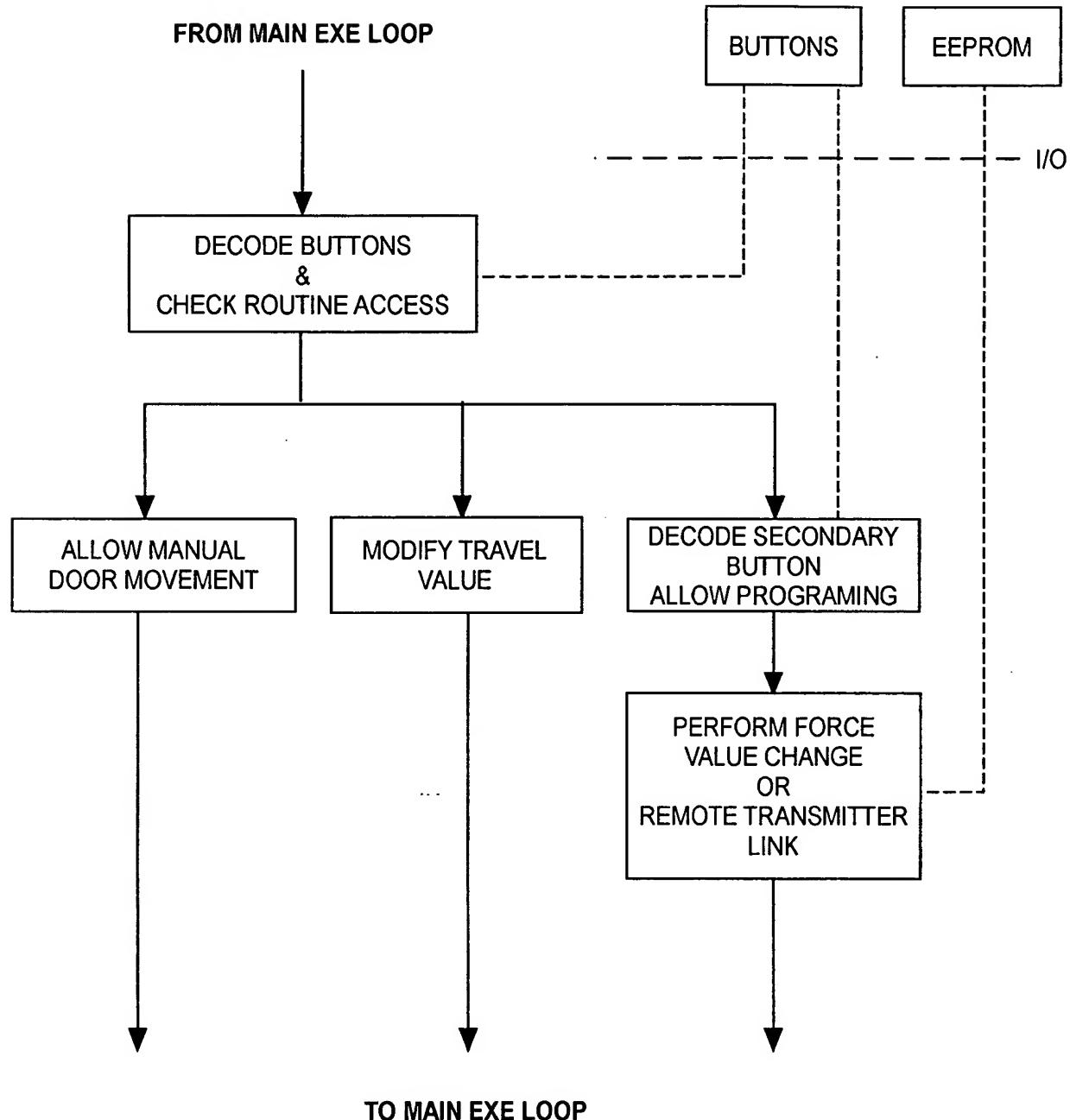
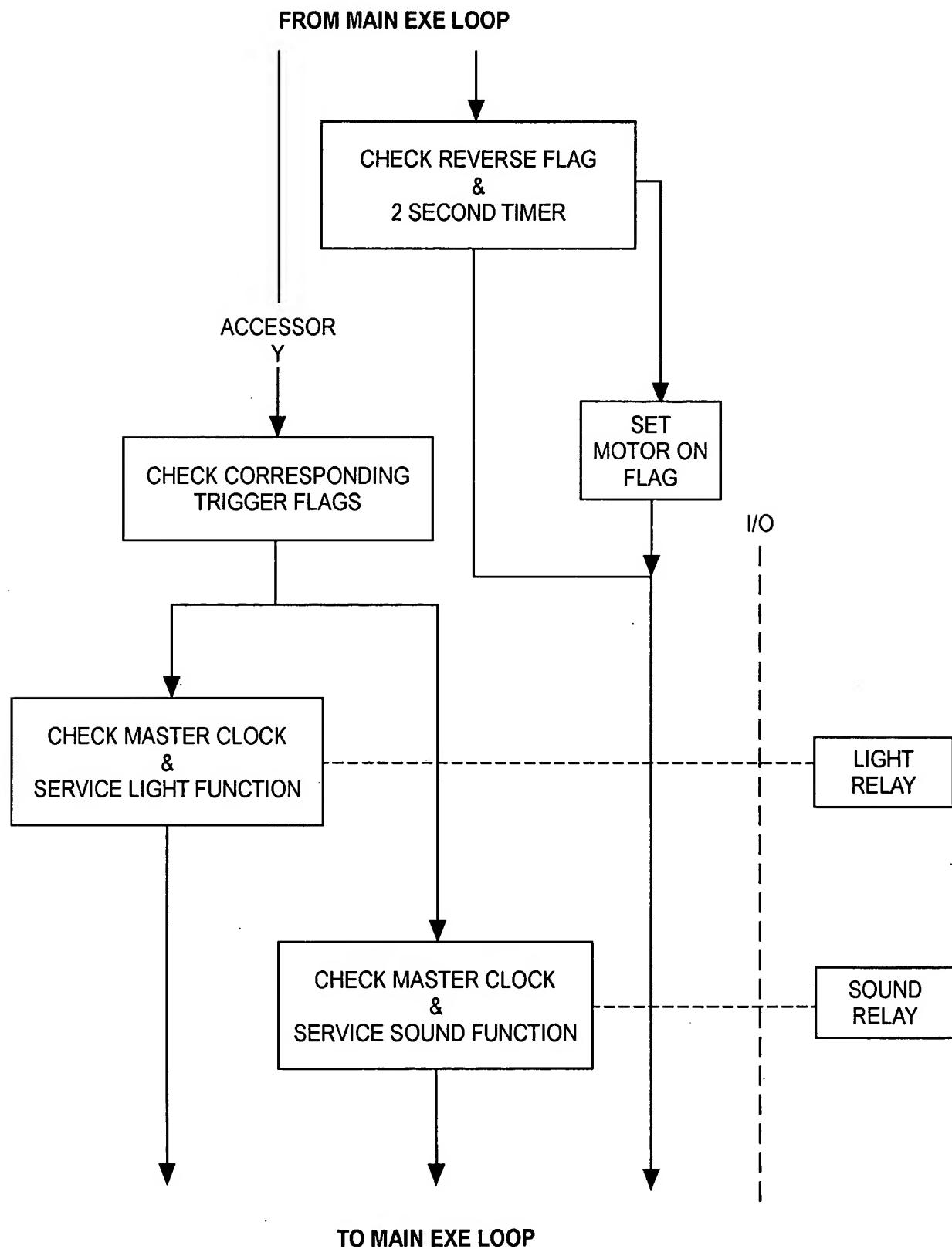


Fig. 28



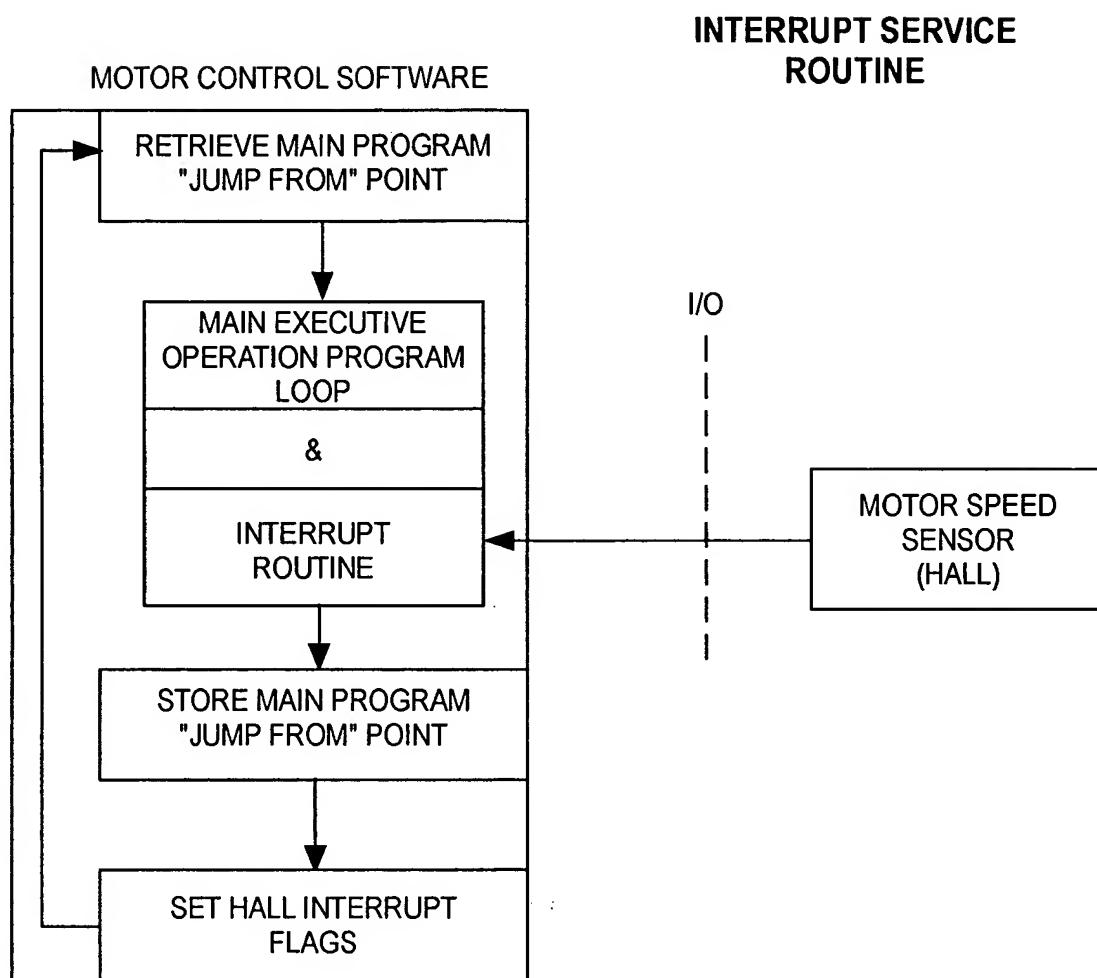
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Fig. 29



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Fig. 30



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Fig. 31

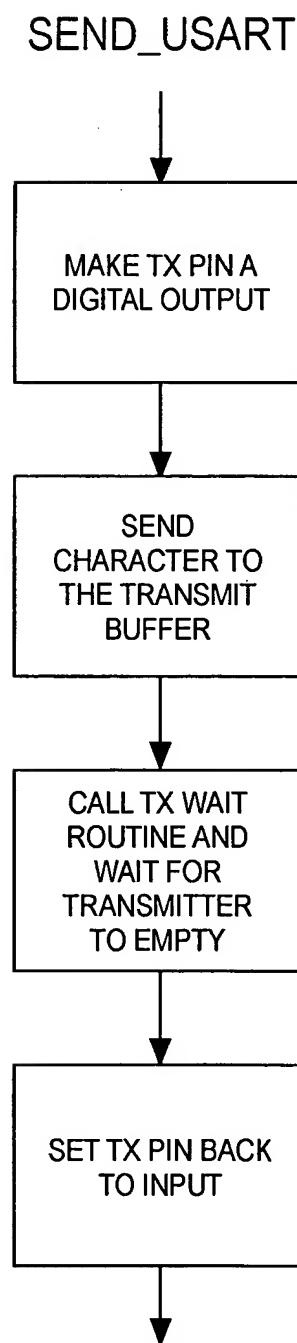
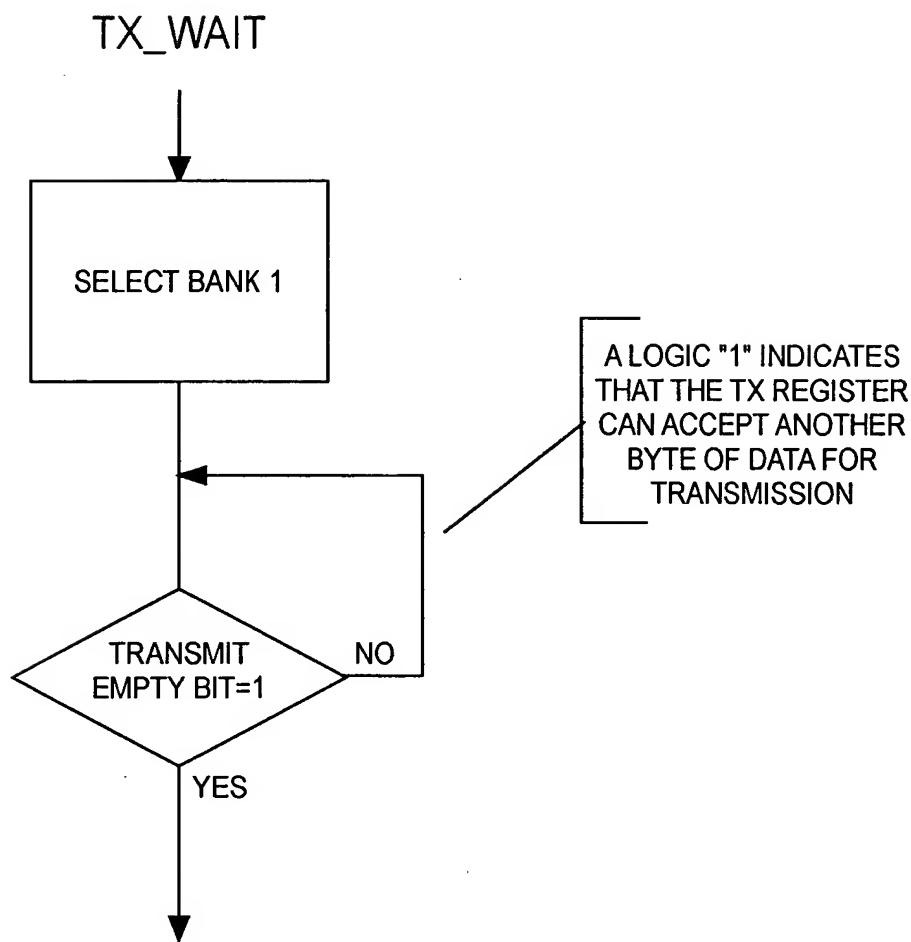


Fig. 32



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Fig. 33

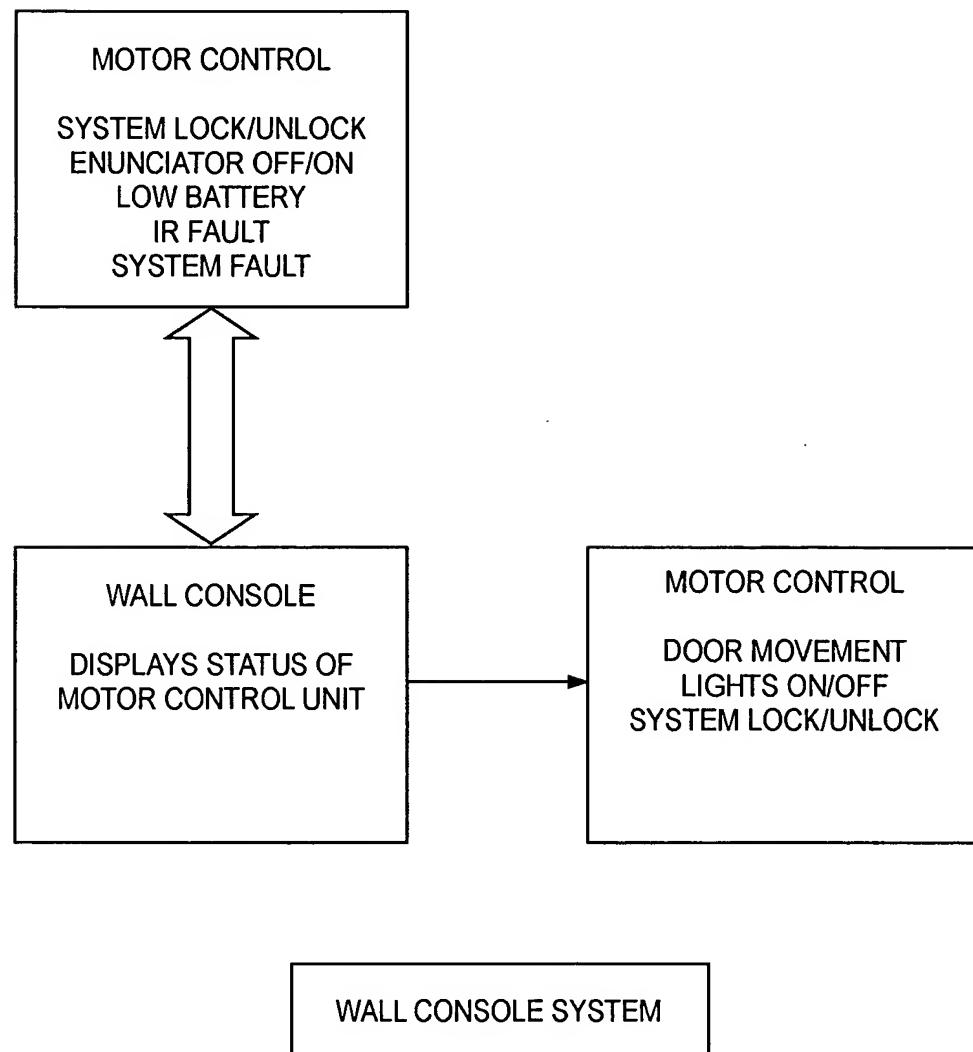
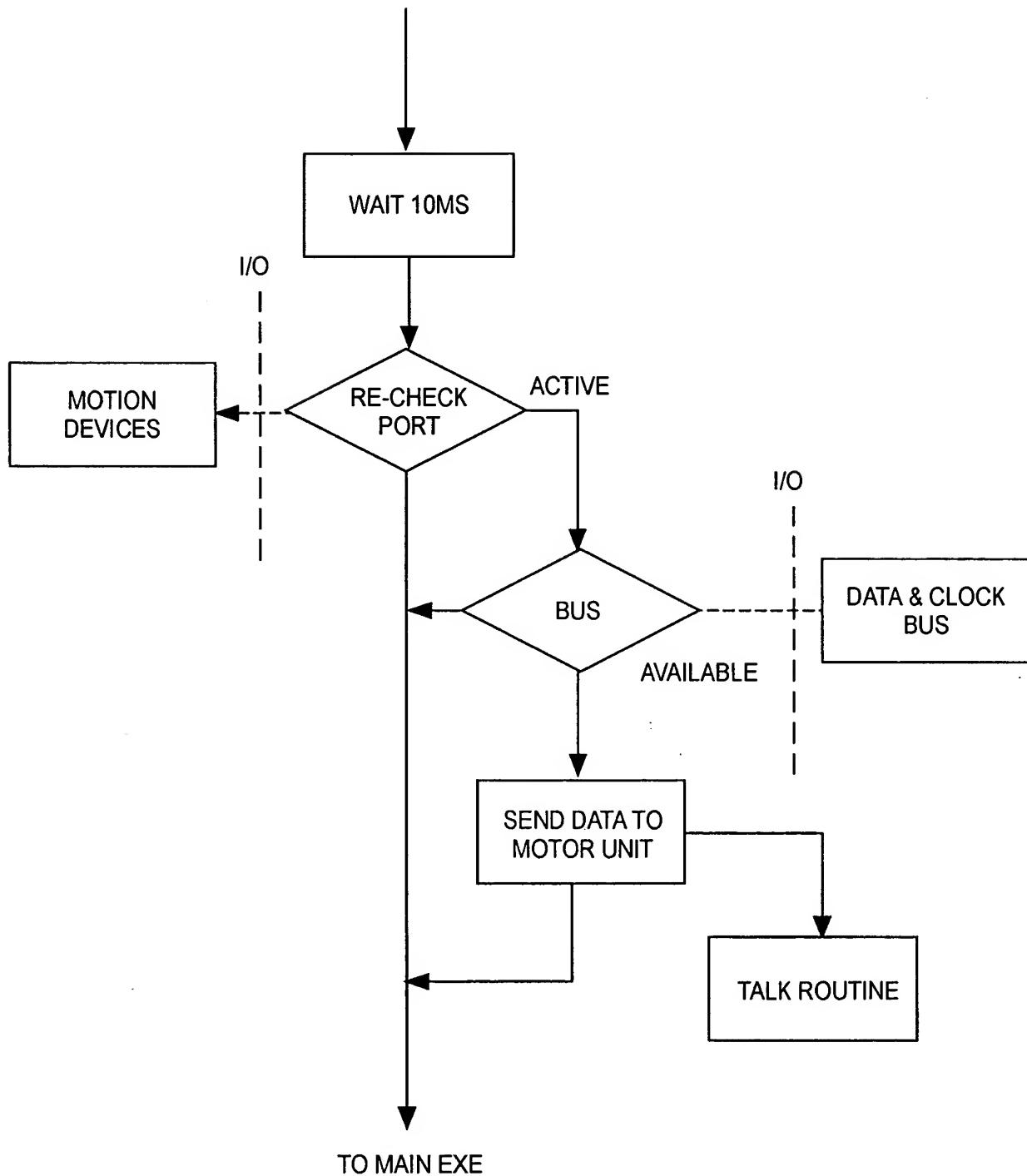


Fig. 34



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Fig. 35

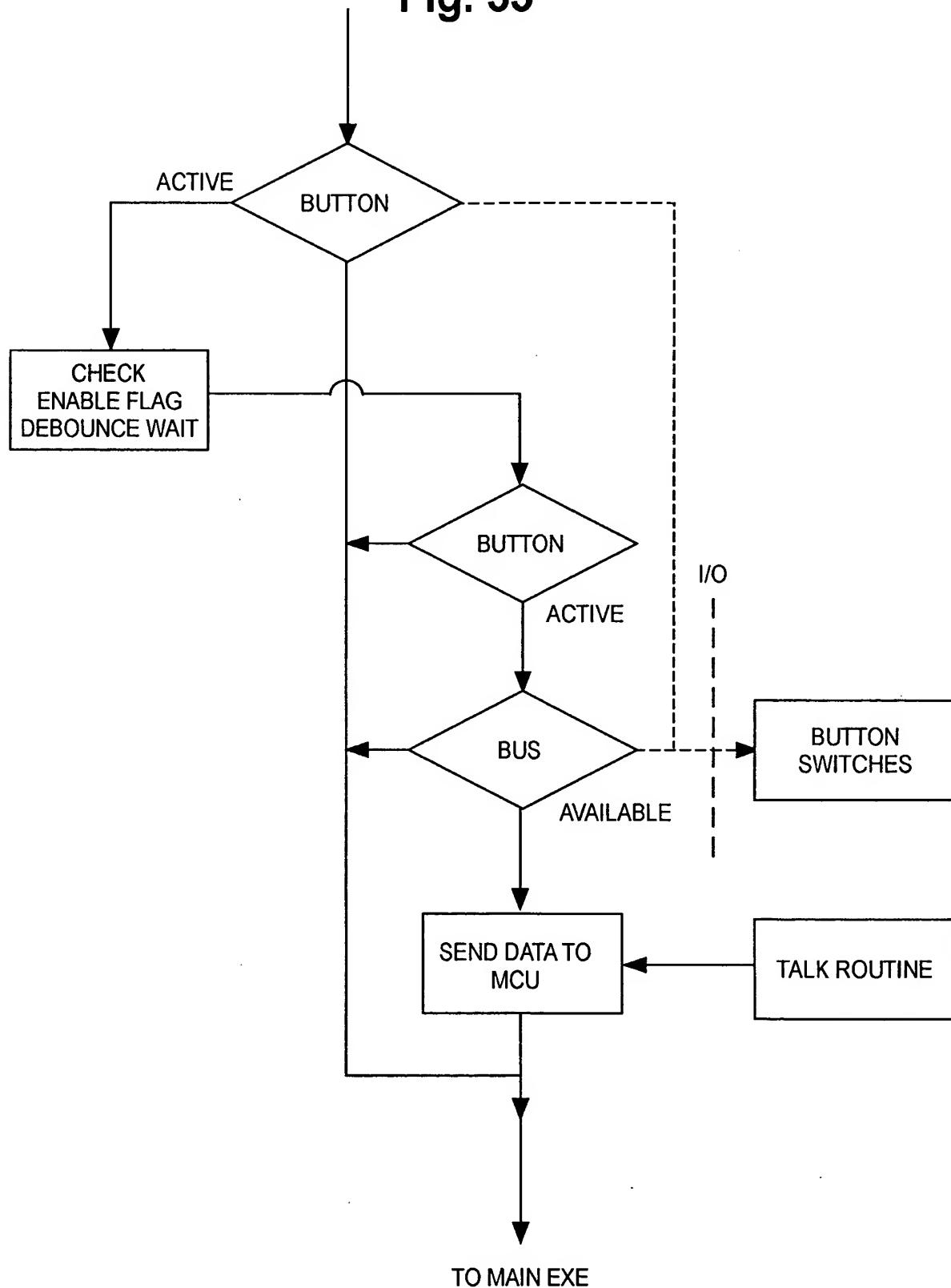


Fig. 36

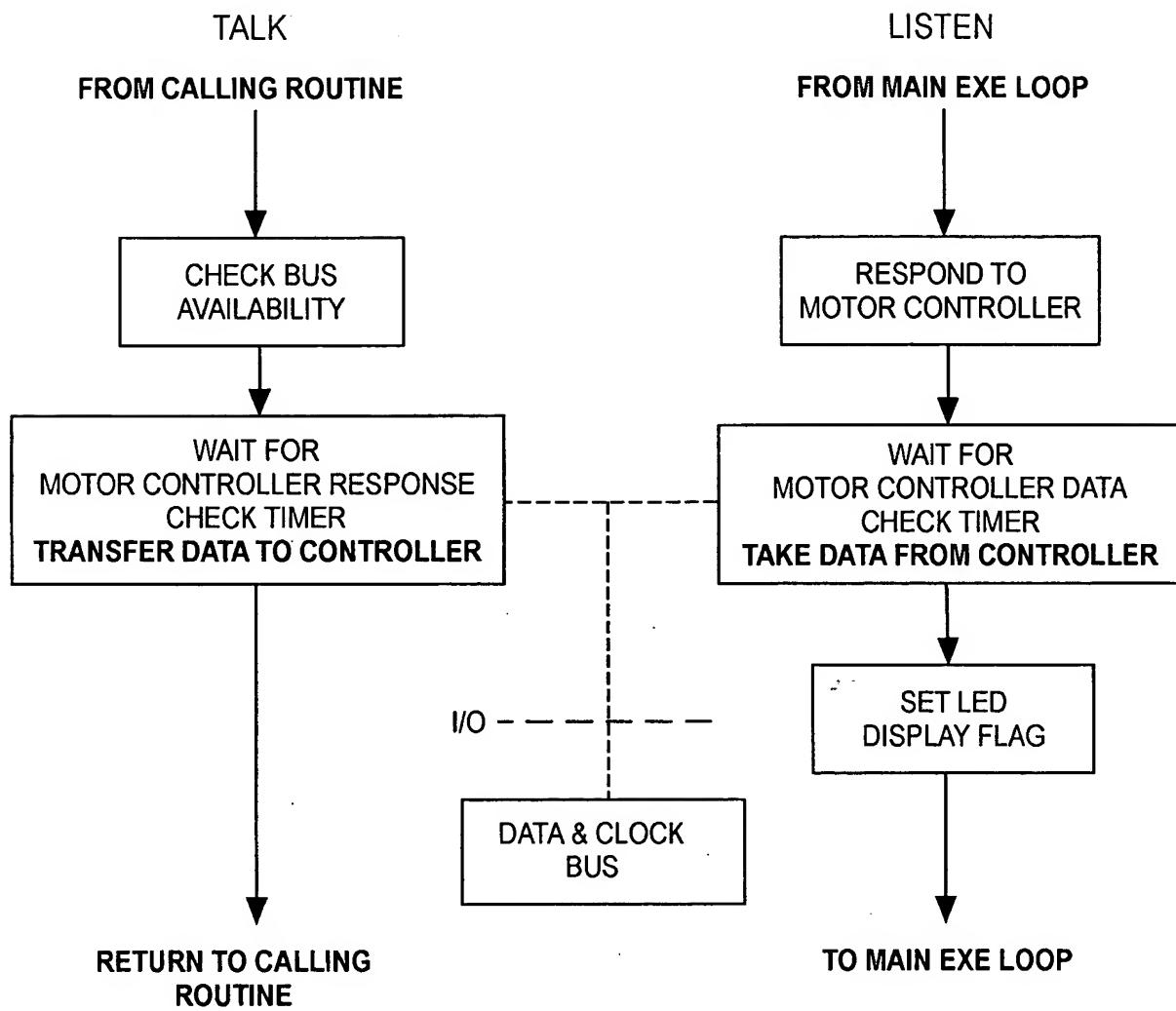


Fig. 37

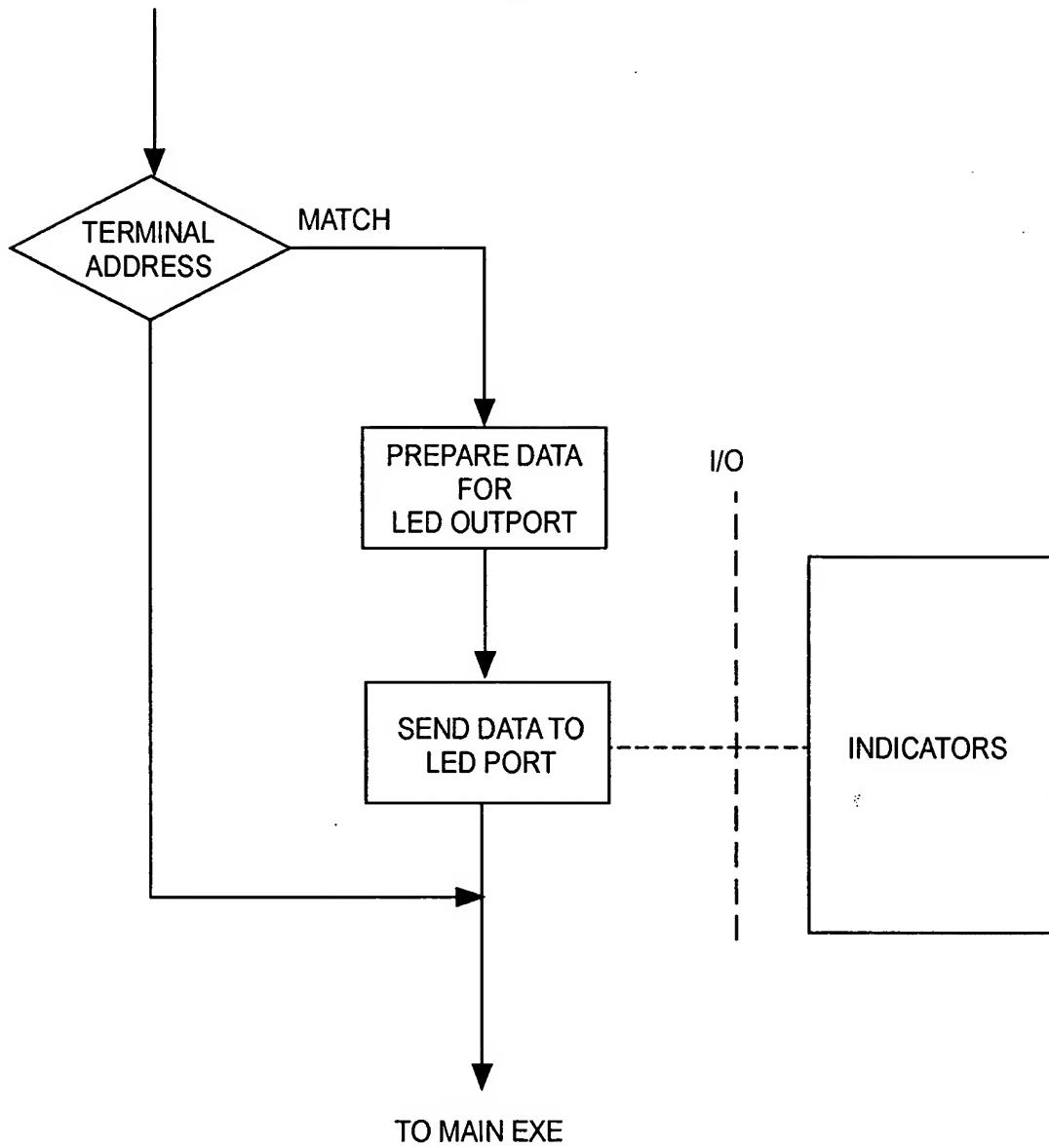
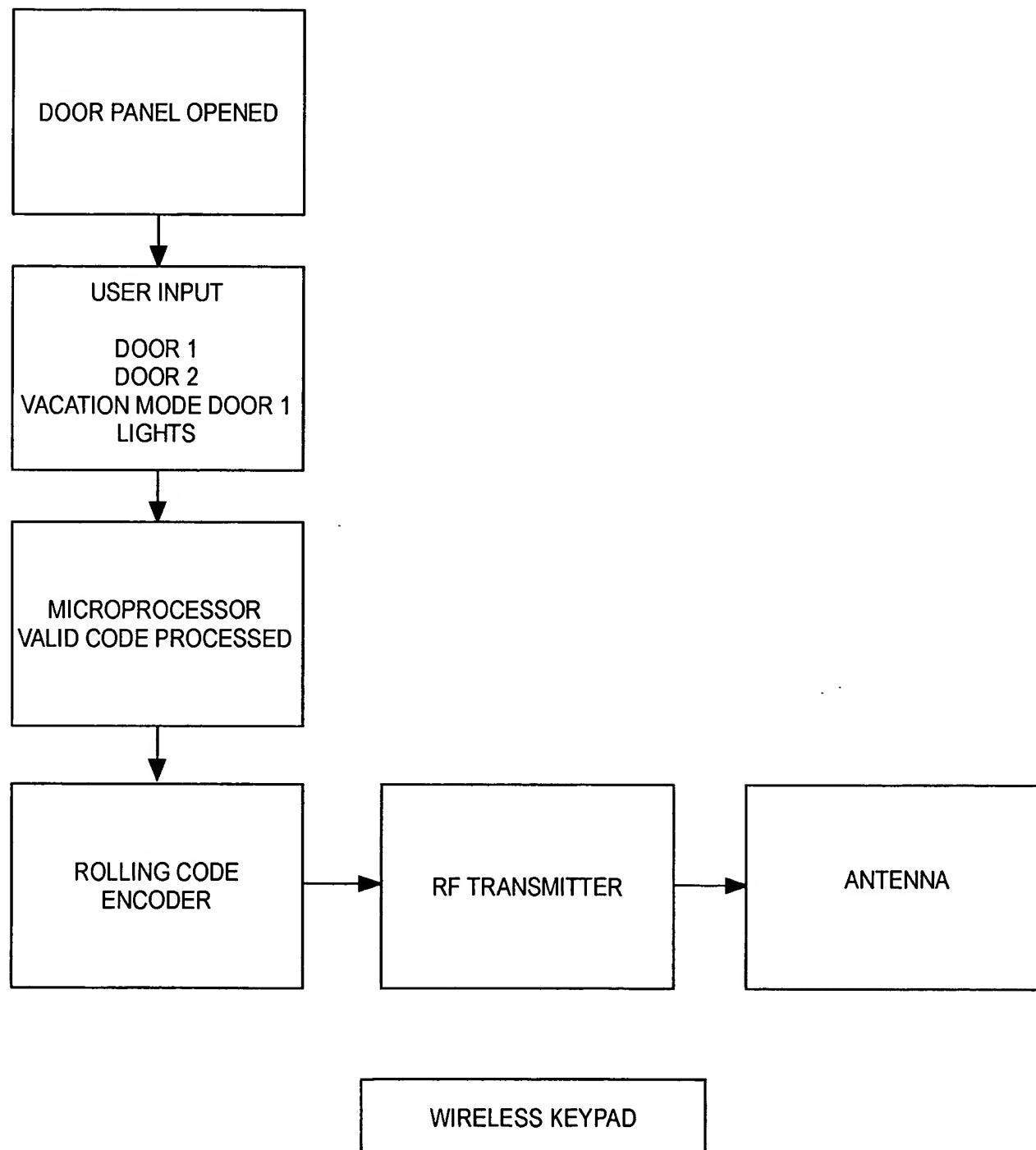


Fig. 38



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Fig. 39

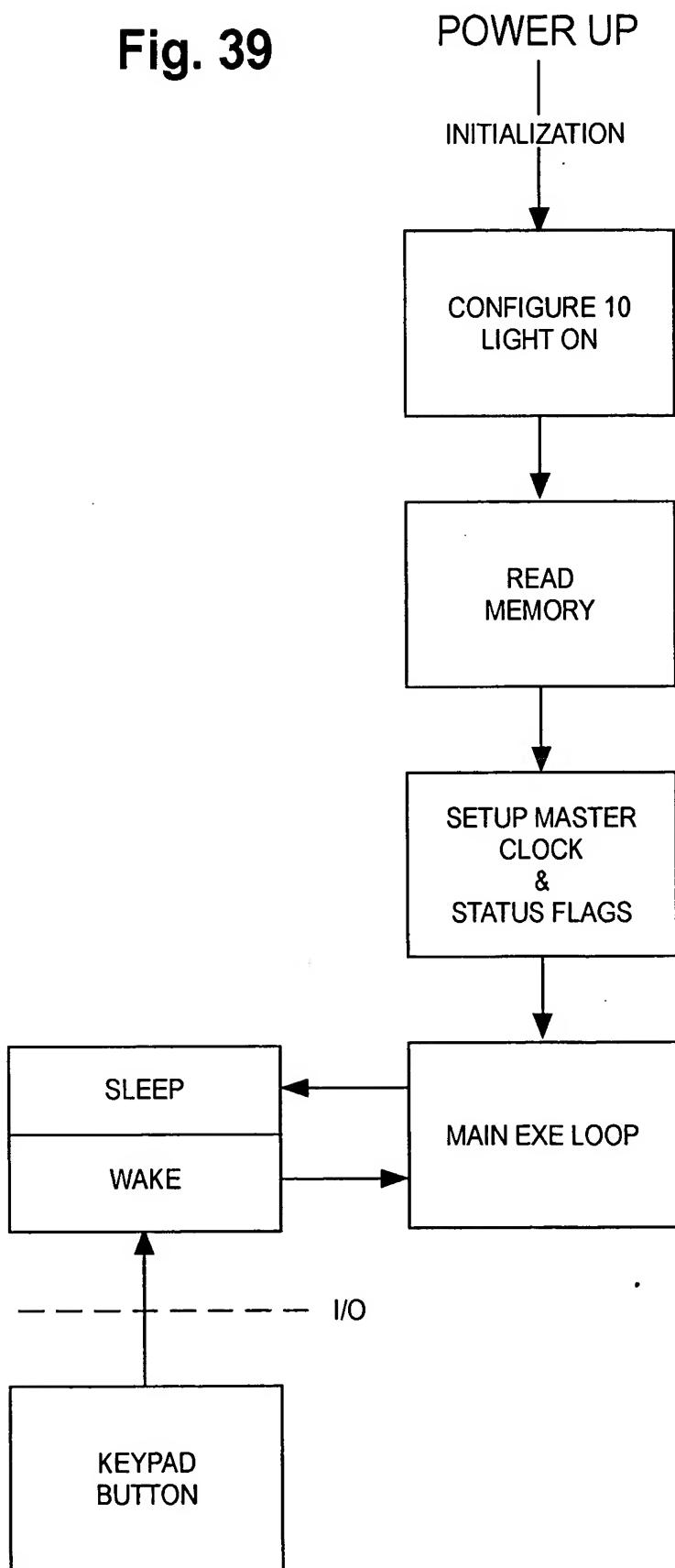


Fig. 40

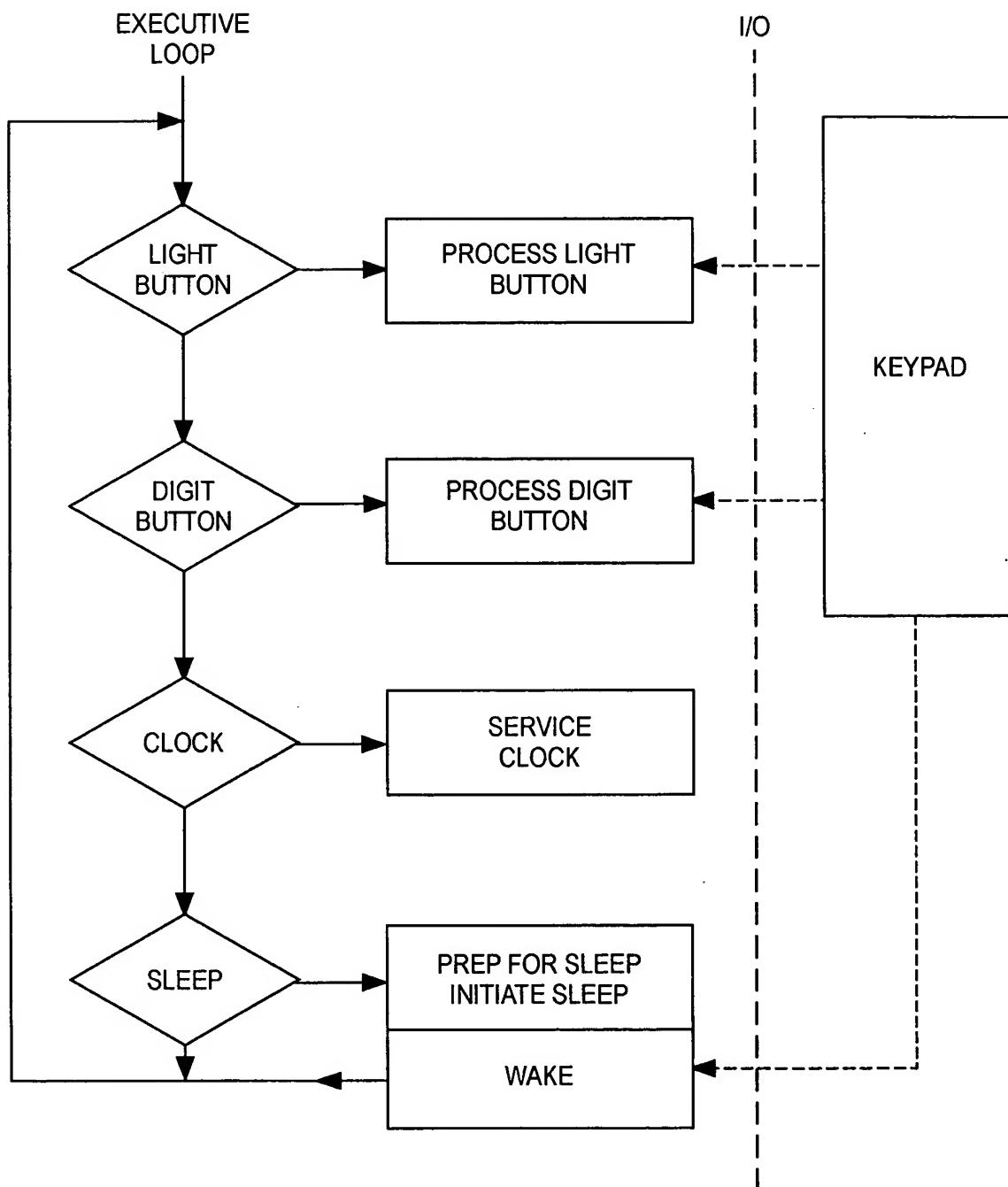


Fig. 41

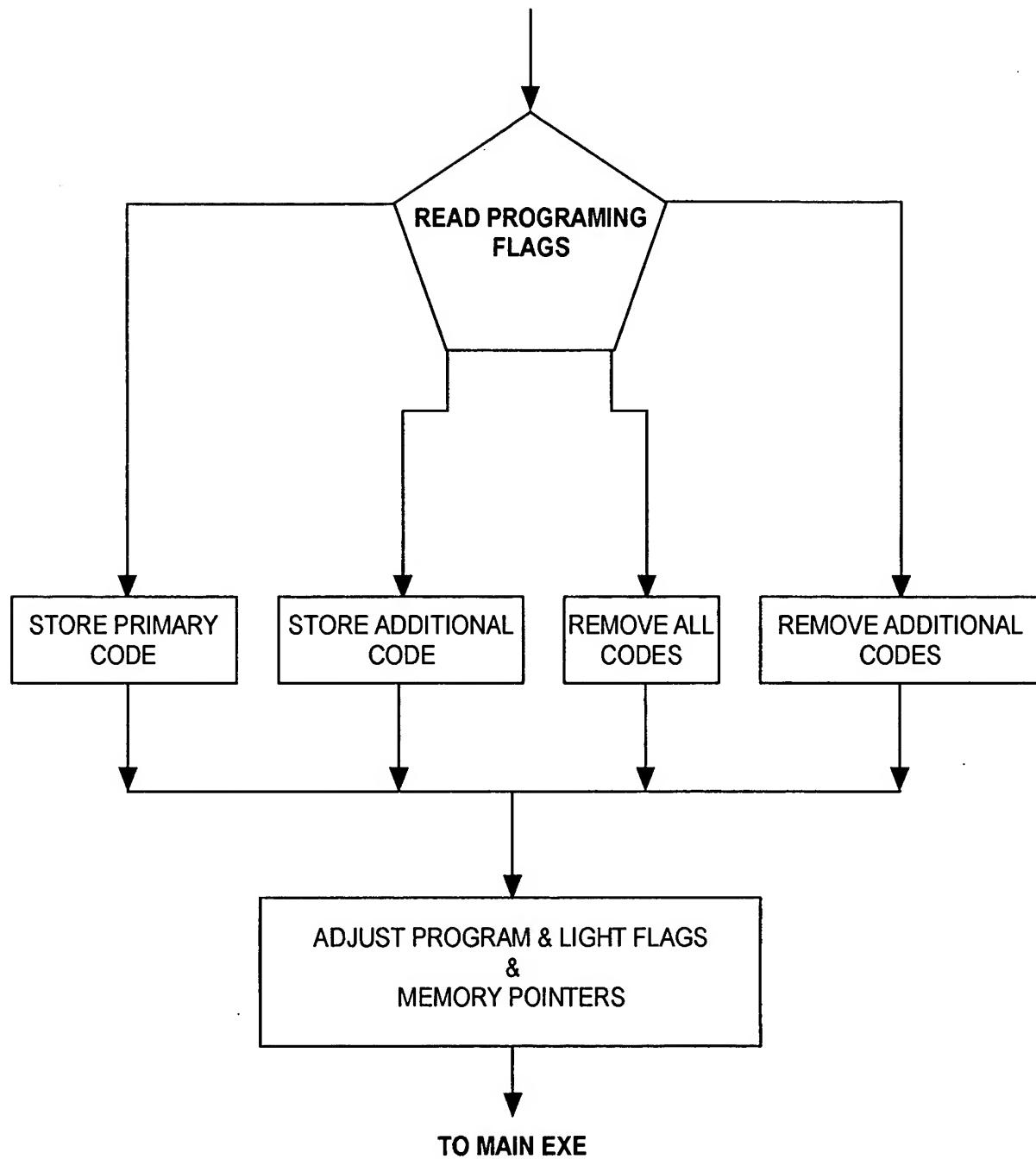


Fig. 42

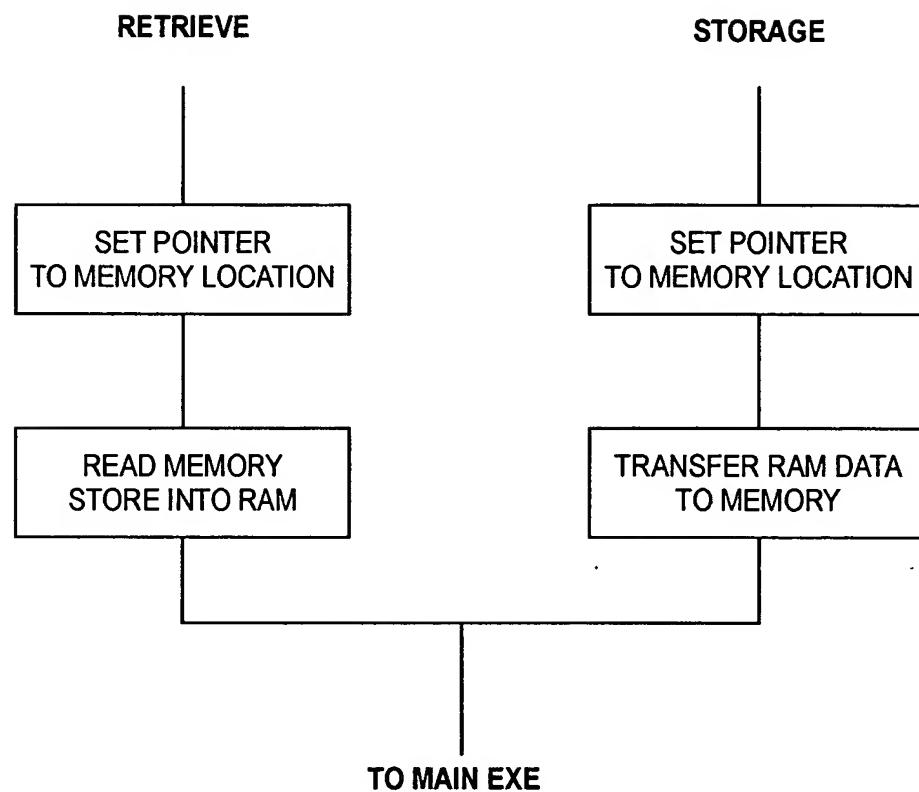


Fig. 43

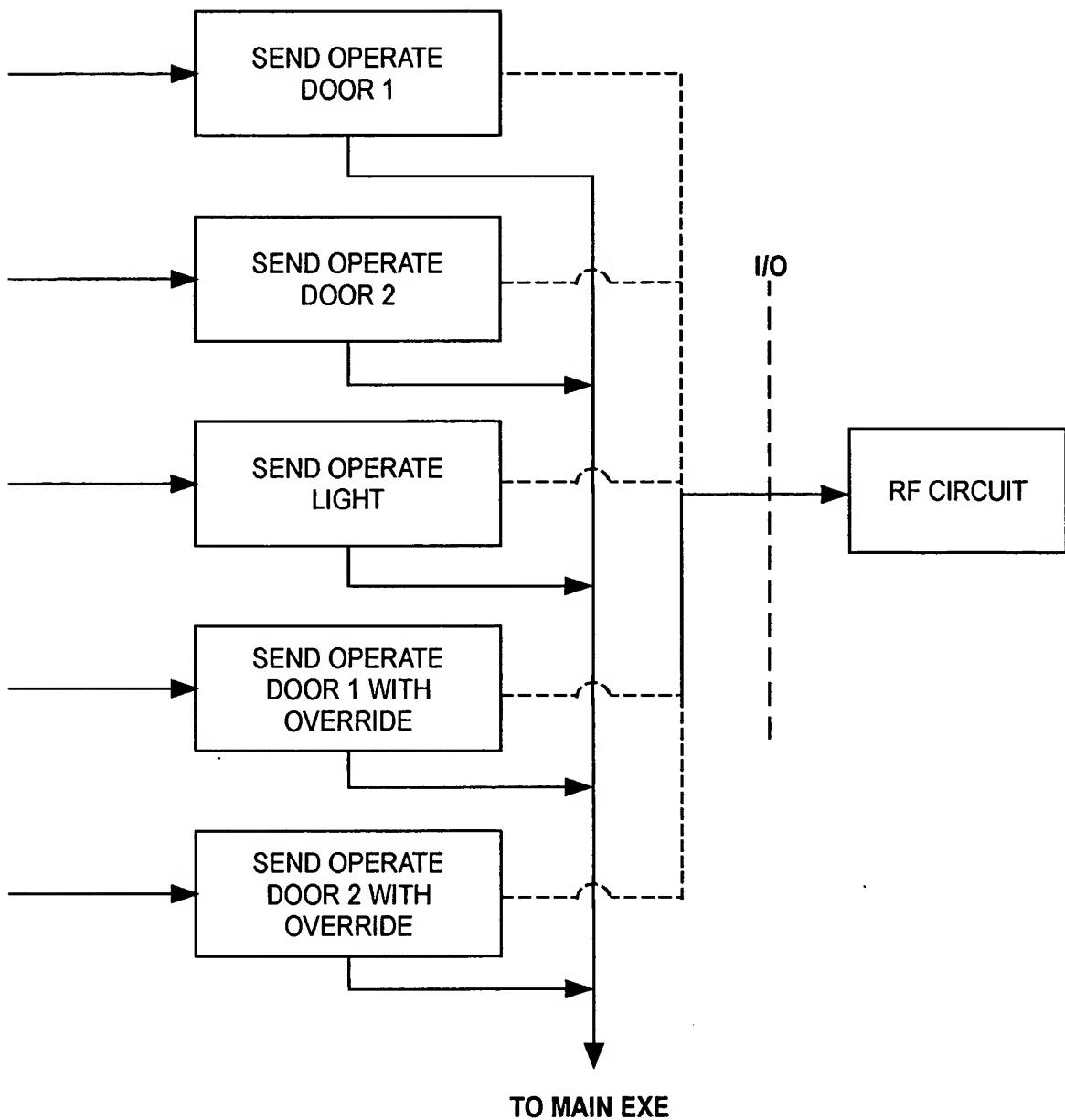


Fig. 44

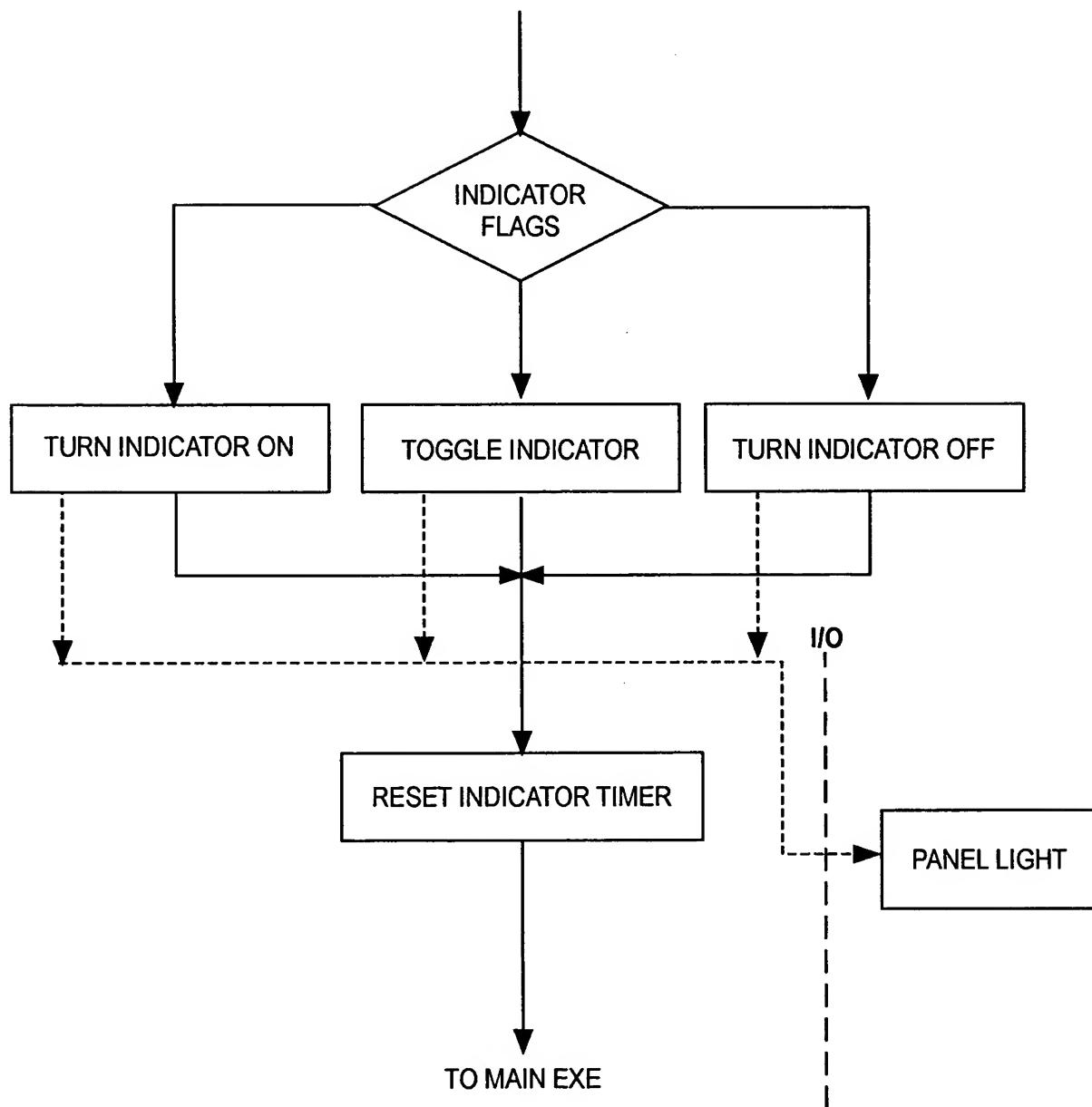


Fig. 45

